

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023001 AAKASH KEWALRAMANI NANDA KEWALRAMANI ,71526367G ,MITP ,B120023001

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	33	46	P	C	404189	MOBILE COMMUNICATION	PP	100	40	18	53	71	P	P
404182	COMPUTER NETWORKS	PP	100	40	17	37	54	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	47	72	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	18	42	60	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	47	66	P	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	24	44	68	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	20	47	67	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			42	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			34	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P	C	404195	PROJECT PHASE II	TW	100	40			97	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			28	P	C	404195	PROJECT PHASE II	PR	50	20			45	P	P
404188	PROJECT PHASE I	OR	50	20			43	P	C	414464D	INTERNET OF THING	PP	100	40	23	45	68	P	P

GRAND TOTAL = 1043/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023002 AGARWAL DIVYANSHU MUKESH SEEMA AGARWAL ,71400020F ,MITP ,B120023002

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	08	32	40	P		404189	MOBILE COMMUNICATION	PP	100	40	18	47	65	P	P
404182	COMPUTER NETWORKS	PP	100	40	11	29	40	P		404190	BROADBAND COMMUNICATION SYS.	PP	100	40	12	43	55	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	12	34	46	P	C	404191D	SOFT COMPUTING	PP	100	40	08	33	41	P	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	16	40	56	P	C	404192D	WIRELESS NETWORKS	PP	100	40	17	18	--	F	F
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	05	47	52	P		404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			34	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			24	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			25	P	C	404195	PROJECT PHASE II	TW	100	40			79	P	P
404188	PROJECT PHASE I	OR	50	20			35	P	C	404195	PROJECT PHASE II	PR	50	20			40	P	P

GRAND TOTAL = --/1500 , RESULT:FAILS [\$ 0.1]

B120023003 AHER DARSHANA MANIK SEEMA ,71526391K ,MITP ,B120023003

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	51	61	P	C	404189	MOBILE COMMUNICATION	PP	100	40	24	53	77	P	P
404182	COMPUTER NETWORKS	PP	100	40	20	30	50	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	46	72	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	22	51	73	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	20	46	66	P	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	27	45	72	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	16	28	44	P	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	20	40	60	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			30	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			25	P	C	404195	PROJECT PHASE II	TW	100	40			80	P	P
404188	PROJECT PHASE I	OR	50	20			38	P	C	404195	PROJECT PHASE II	PR	50	20			38	P	P

GRAND TOTAL = 1013/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023004 AINWAD SUNILKUMAR SAMBHAJI PARVATI ,71400028M ,MITP ,B120023004

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	35	52	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	30	46	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	24	38	62	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	24	40	64	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	17	38	55	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C
404188	PROJECT PHASE I	OR	50	20			36	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	23	57	80	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	18	49	67	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	46	65	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			35	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			33	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			34	P	P
404195	PROJECT PHASE II	TW	100	40			92	P	P
404195	PROJECT PHASE II	PR	50	20			43	P	P
414464D	INTERNET OF THING	PP	100	40	24	41	65	P	P

GRAND TOTAL = 1017/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023005 AKANKSHA SUNALKAR RAJASHREE ,71526395B ,MITP ,B120023005

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	39	57	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	31	54	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	44	65	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	25	50	75	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	23	49	72	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	23	55	78	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	18	55	73	P	P
404191D	SOFT COMPUTING	PP	100	40	29	42	71	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			46	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			45	P	P
404195	PROJECT PHASE II	TW	100	40			95	P	P
404195	PROJECT PHASE II	PR	50	20			48	P	P
414464D	INTERNET OF THING	PP	100	40	30	41	71	P	P

GRAND TOTAL = 1142/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023006 ANAND RAVI VANMATHI ,71526406M ,MITP ,B120023006

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	37	50	P	C
404182	COMPUTER NETWORKS	PP	100	40	24	31	55	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	30	49	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	29	48	77	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	26	46	72	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C
404188	PROJECT PHASE I	OR	50	20			45	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	27	48	75	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	55	78	P	P
404191D	SOFT COMPUTING	PP	100	40	21	41	62	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			44	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			41	P	P
404195	PROJECT PHASE II	TW	100	40			95	P	P
404195	PROJECT PHASE II	PR	50	20			48	P	P
414464D	INTERNET OF THING	PP	100	40	28	48	76	P	P

GRAND TOTAL = 1105/1500 , RESULT:FIRST CLASS WITH DISTINCTION

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B120023007 ANANDITA SAHU ANURADHA ,71526407K ,MITP ,B120023007

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	49	67	P	C	404189	MOBILE COMMUNICATION	PP	100	40	24	57	81	P	P
404182	COMPUTER NETWORKS	PP	100	40	16	36	52	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	28	57	85	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	22	50	72	P	C	404191D	SOFT COMPUTING	PP	100	40	25	43	68	P	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	29	52	81	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	19	28	47	P	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	18	51	69	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			46	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			46	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C	404195	PROJECT PHASE II	TW	100	40			95	P	P
404188	PROJECT PHASE I	OR	50	20			46	P	C	404195	PROJECT PHASE II	PR	50	20			45	P	P

GRAND TOTAL = 1142/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023008 ANAY TARE DEEPA ,71526408H ,MITP ,B120023008

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	38	49	P	C	404189	MOBILE COMMUNICATION	PP	100	40	18	58	76	P	P
404182	COMPUTER NETWORKS	PP	100	40	17	34	51	P	P	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	13	51	64	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	13	29	42	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	46	64	P	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	19	56	75	P	C	404192D	WIRELESS NETWORKS	PP	100	40	20	48	68	P	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	12	35	47	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	P	404195	PROJECT PHASE II	TW	100	40			90	P	P
404188	PROJECT PHASE I	OR	50	20			39	P	C	404195	PROJECT PHASE II	PR	50	20			44	P	P

GRAND TOTAL = 1021/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023009 ANURAG KUMAR POONAM ,71526414B ,MITP ,B120023009

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	01	41	42	P	P	404189	MOBILE COMMUNICATION	PP	100	40	18	52	70	P	P
404182	COMPUTER NETWORKS	PP	100	40	12	31	43	P	P	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	16	40	56	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	08	34	42	P	P	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	16	48	64	P	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	18	47	65	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	16	29	45	P	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	16	51	67	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			44	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C	404195	PROJECT PHASE II	TW	100	40			97	P	P
404188	PROJECT PHASE I	OR	50	20			45	P	C	404195	PROJECT PHASE II	PR	50	20			47	P	P

GRAND TOTAL = 983/1500 , RESULT:FIRST CLASS

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.  
 OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023010 APURVA JANARDAN DAPHAL LATIKA ,71526416J ,MITP ,B120023010

SEM.:1								SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	07	36	43	P C	404189	MOBILE COMMUNICATION	PP	100	40	25	48	73	P
404182	COMPUTER NETWORKS	PP	100	40	22	37	59	P C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	21	45	66	P
404183	MICROWAVE ENGINEERING	PP	100	40	13	46	59	P C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	46	61	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	16	52	68	P C	404192D	WIRELESS NETWORKS	PP	100	40	14	37	51	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	19	43	62	P C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P C	404195	PROJECT PHASE II	TW	100	40			96	P
404188	PROJECT PHASE I	OR	50	20			37	P C	404195	PROJECT PHASE II	PR	50	20			47	P

GRAND TOTAL = 1028/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023011 ARWALKAR AMAR ASHOK SHASHIKALA ,71633534E ,MITP ,B120023011

SEM.:1								SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	07	28	--	F	404189	MOBILE COMMUNICATION	PP	100	40	18	40	58	P
404182	COMPUTER NETWORKS	PP	100	40	15	31	46	P	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	17	47	64	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	36	55	P C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	42	57	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	24	41	65	P C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	16	18	--	F
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	12	37	49	P C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			33	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P C	404195	PROJECT PHASE II	TW	100	40			78	P
404188	PROJECT PHASE I	OR	50	20			37	P C	404195	PROJECT PHASE II	PR	50	20			41	P

GRAND TOTAL = --/1500 , RESULT:FAILS

B120023012 ASTHA SHRIVASTAVA DEEPTI SHRIVASTAVA ,71400063K ,MITP ,B120023012

SEM.:1								SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	44	57	P	404189	MOBILE COMMUNICATION	PP	100	40	18	46	64	P
404182	COMPUTER NETWORKS	PP	100	40	10	33	43	P	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	09	48	57	P
404183	MICROWAVE ENGINEERING	PP	100	40	AA	40	40	P	404191D	SOFT COMPUTING	PP	100	40	15	42	57	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	AA	57	57	P	404192D	WIRELESS NETWORKS	PP	100	40	12	36	48	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	AA	64	64	P	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			34	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			25	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	404195	PROJECT PHASE II	TW	100	40			91	P
404188	PROJECT PHASE I	OR	50	20			42	P C	404195	PROJECT PHASE II	PR	50	20			44	P

GRAND TOTAL = 947/1500 , RESULT:FIRST CLASS

RESULT RESERVED FOR BACKLOGS.

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023013 BANDARU KRISHNA MURTHY PARVATHI ,71526439H ,MITP ,B120023013

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	39	58	P	C
404182	COMPUTER NETWORKS	PP	100	40	20	35	55	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	26	45	71	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	23	49	72	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	21	31	52	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	23	48	71	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	27	45	72	P	P
404191D	SOFT COMPUTING	PP	100	40	22	39	61	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			43	P	P
404195	PROJECT PHASE II	TW	100	40			91	P	P
404195	PROJECT PHASE II	PR	50	20			42	P	P
414464D	INTERNET OF THING	PP	100	40	28	32	60	P	P

GRAND TOTAL = 1052/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023014 BARAD AKSHAY GAJANAN CHHAYA ,71633535C ,MITP ,B120023014

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	24	59	83	P	C
404182	COMPUTER NETWORKS	PP	100	40	13	34	47	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	27	44	71	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	26	59	85	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	20	50	70	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	21	46	67	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	53	77	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	23	44	67	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	22	28	50	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	P
404195	PROJECT PHASE II	TW	100	40			90	P	P
404195	PROJECT PHASE II	PR	50	20			45	P	P

GRAND TOTAL = 1084/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023015 BHALERAO SANTOSHI JITENDRA JYOTI ,71633536M ,MITP ,B120023015

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	55	71	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	35	52	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	12	39	51	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	16	56	72	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	13	43	56	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			47	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			31	P	C
404188	PROJECT PHASE I	OR	50	20			37	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	51	69	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	45	65	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	49	64	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	15	28	43	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	P
404195	PROJECT PHASE II	TW	100	40			78	P	P
404195	PROJECT PHASE II	PR	50	20			40	P	P

GRAND TOTAL = 1007/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023016 BHIDE ADWAIET VINAYAK MANJUSHA ,71526457F ,MITP ,B120023016

SEM.:1							SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	35	46	P	C	404189	MOBILE COMMUNICATION	PP	100	40	27	44	71	P
404182	COMPUTER NETWORKS	PP	100	40	18	31	49	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	13	44	57	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	43	64	P	C	404191D	SOFT COMPUTING	PP	100	40	21	36	57	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	21	44	65	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	21	46	67	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			36	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P	C	404195	PROJECT PHASE II	TW	100	40			88	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P	C	404195	PROJECT PHASE II	PR	50	20			44	P
404188	PROJECT PHASE I	OR	50	20			43	P	C	414464D	INTERNET OF THING	PP	100	40	29	48	77	P

GRAND TOTAL = 1043/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023017 BHINGARDE MAYURA VIJAY SUNITA ,71526459B ,MITP ,B120023017

SEM.:1							SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	64	79	P	C	404189	MOBILE COMMUNICATION	PP	100	40	22	62	84	P
404182	COMPUTER NETWORKS	PP	100	40	26	40	66	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	29	55	84	P
404183	MICROWAVE ENGINEERING	PP	100	40	23	49	72	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	24	51	75	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	24	64	88	P	C	404192D	WIRELESS NETWORKS	PP	100	40	19	54	73	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	28	56	84	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			45	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			46	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C	404195	PROJECT PHASE II	TW	100	40			97	P
404188	PROJECT PHASE I	OR	50	20			42	P	C	404195	PROJECT PHASE II	PR	50	20			48	P

GRAND TOTAL = 1221/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023018 CHAUDHARI VASUDHA JAYWANT NEETA ,71526484C ,MITP ,B120023018

SEM.:1							SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	51	61	P	C	404189	MOBILE COMMUNICATION	PP	100	40	17	49	66	P
404182	COMPUTER NETWORKS	PP	100	40	15	40	55	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	13	49	62	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	47	67	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	21	47	68	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	27	43	70	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	12	38	50	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P	C	404195	PROJECT PHASE II	TW	100	40			91	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			41	P	C	404195	PROJECT PHASE II	PR	50	20			42	P
404188	PROJECT PHASE I	OR	50	20			38	P	C	414464D	INTERNET OF THING	PP	100	40	29	45	74	P

GRAND TOTAL = 1058/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023019 CHAVAN SIDDESH SUNIL MANISHA ,71633537K ,MITP ,B120023019

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	52	71	P	C
404182	COMPUTER NETWORKS	PP	100	40	24	41	65	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	51	73	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	28	62	90	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	25	53	78	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			30	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			28	P	C
404188	PROJECT PHASE I	OR	50	20			46	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	19	46	65	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	16	54	70	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	17	44	61	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	22	28	50	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			43	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			44	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			42	P	
404195	PROJECT PHASE II	TW	100	40			98	P	
404195	PROJECT PHASE II	PR	50	20			46	P	

GRAND TOTAL = 1118/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023020 CHAVAN TANUSHREE RAMDAS SUREKHA ,71526490H ,MITP ,B120023020

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	49	64	P	C
404182	COMPUTER NETWORKS	PP	100	40	13	28	41	P	
404183	MICROWAVE ENGINEERING	PP	100	40	16	38	54	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	18	45	63	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	16	47	63	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	21	53	74	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	19	56	75	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	20	47	67	P	
404192D	WIRELESS NETWORKS	PP	100	40	22	38	60	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			42	P	
404195	PROJECT PHASE II	TW	100	40			92	P	
404195	PROJECT PHASE II	PR	50	20			45	P	

GRAND TOTAL = 1055/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023021 DABARE SHWETA BABAN VEENA ,71526499M ,MITP ,B120023021

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	51	64	P	C
404182	COMPUTER NETWORKS	PP	100	40	18	36	54	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	18	44	62	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	29	52	81	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	22	49	71	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			28	P	C
404188	PROJECT PHASE I	OR	50	20			37	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	21	45	66	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	55	78	P	
404191D	SOFT COMPUTING	PP	100	40	25	46	71	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	24	30	54	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	
404195	PROJECT PHASE II	TW	100	40			80	P	
404195	PROJECT PHASE II	PR	50	20			37	P	

GRAND TOTAL = 1056/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023023 DELIYA NIKHIL NARESH GEETA ,71526512B ,MITP ,B120023023

SEM.:1								SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	38	51	P	C	404189	MOBILE COMMUNICATION	PP	100	40	18	46	64	P	P
404182	COMPUTER NETWORKS	PP	100	40	14	28	42	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	13	43	56	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	26	35	61	P	C	404191D	SOFT COMPUTING	PP	100	40	21	43	64	P	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	24	33	57	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	13	31	44	P	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	16	41	57	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			46	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C	404195	PROJECT PHASE II	TW	100	40			92	P	P
404188	PROJECT PHASE I	OR	50	20			43	P	C	404195	PROJECT PHASE II	PR	50	20			44	P	P

GRAND TOTAL = 989/1500 , RESULT:FIRST CLASS

B120023024 DESHMUKH SHUBHAM ANIL SAVITA ,71526521M ,MITP ,B120023024

SEM.:1								SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	00	20	--	F		404189	MOBILE COMMUNICATION	PP	100	40	12	39	51	P	P
404182	COMPUTER NETWORKS	PP	100	40	14	10	--	F		404190	BROADBAND COMMUNICATION SYS.	PP	100	40	16	31	47	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	11	31	42	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	36	49	P	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	11	36	47	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			35	P	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	13	32	45	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			35	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			34	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			32	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C	404195	PROJECT PHASE II	TW	100	40			95	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			41	P		404195	PROJECT PHASE II	PR	50	20			45	P	P
404188	PROJECT PHASE I	OR	50	20			30	P		414464D	INTERNET OF THING	PP	100	40	17	33	50	P	P

GRAND TOTAL = --/1500 , RESULT:FAILS

RESULT RESERVED FOR BACKLOGS.

B120023025 DHAIGUDE PAYAL SAMPAT VIJAYA ,71633538H ,MITP ,B120023025

SEM.:1								SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	56	73	P	C	404189	MOBILE COMMUNICATION	PP	100	40	27	46	73	P	P
404182	COMPUTER NETWORKS	PP	100	40	17	35	52	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	60	80	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	27	35	62	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	25	51	76	P	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	30	64	94	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	25	35	60	P	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	10	49	59	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			44	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			42	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			41	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C	404195	PROJECT PHASE II	TW	100	40			96	P	P
404188	PROJECT PHASE I	OR	50	20			43	P	C	404195	PROJECT PHASE II	PR	50	20			47	P	P

GRAND TOTAL = 1139/1500 , RESULT:FIRST CLASS WITH DISTINCTION



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023026 DHANVIJ ANKITA DILIP USHA ,71526529G ,MITP ,B120023026

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	08	45	53	P	C
404182	COMPUTER NETWORKS	PP	100	40	15	35	50	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	16	36	52	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	27	54	81	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	08	38	46	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C
404188	PROJECT PHASE I	OR	50	20			36	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	54	71	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	59	81	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	25	46	71	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	18	20	--	F	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			79	P	
404195	PROJECT PHASE II	PR	50	20			37	P	

GRAND TOTAL = --/1500 , RESULT:FAILS

B120023027 DHENDE PRATIK SURESH RAKHEE ,71526533E ,MITP ,B120023027

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	09	47	56	P	C
404182	COMPUTER NETWORKS	PP	100	40	15	31	46	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	14	40	54	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	17	43	60	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	13	29	42	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			22	P	C
404188	PROJECT PHASE I	OR	50	20			37	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	45	63	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	21	52	73	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	45	60	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	17	28	45	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			35	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	
404195	PROJECT PHASE II	TW	100	40			80	P	
404195	PROJECT PHASE II	PR	50	20			41	P	

GRAND TOTAL = 930/1500 , RESULT:FIRST CLASS

B120023028 DIVEKAR POOJA CHANDRAKANT CHANDRAKALA ,71633539F ,MITP ,B120023028

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	56	70	P	C
404182	COMPUTER NETWORKS	PP	100	40	26	40	66	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	45	64	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	22	52	74	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	15	42	57	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	47	65	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	10	33	43	P	P
404191D	SOFT COMPUTING	PP	100	40	13	40	53	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	15	31	46	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			30	P	
404195	PROJECT PHASE II	TW	100	40			90	P	
404195	PROJECT PHASE II	PR	50	20			41	P	

GRAND TOTAL = 1008/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023029 GAIKWAD AKSHAY SAYAJI JYOTI ,71633540K ,MITP ,B120023029

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	59	77	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	44	65	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	26	45	71	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	29	61	90	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	22	51	73	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			48	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			47	P	C
404188	PROJECT PHASE I	OR	50	20			47	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	27	50	77	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	44	70	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	23	49	72	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	24	44	68	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			45	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			42	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			47	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			48	P	P
404195	PROJECT PHASE II	TW	100	40			98	P	P
404195	PROJECT PHASE II	PR	50	20			46	P	P

GRAND TOTAL = 1211/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023030 GANDHI MEET ATUL HEENA ,71526561L ,MITP ,B120023030

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	48	65	P	C
404182	COMPUTER NETWORKS	PP	100	40	20	35	55	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	16	47	63	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	28	48	76	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	17	48	65	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	23	54	77	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	44	66	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	23	52	75	P	P
404192D	WIRELESS NETWORKS	PP	100	40	20	42	62	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			45	P	P
404195	PROJECT PHASE II	TW	100	40			89	P	P
404195	PROJECT PHASE II	PR	50	20			42	P	P

GRAND TOTAL = 1090/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023032 GAWAI ANAND MANGAL RANJANA ,71633541H ,MITP ,B120023032

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	51	62	P	C
404182	COMPUTER NETWORKS	PP	100	40	18	38	56	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	25	43	68	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	25	63	88	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	19	45	64	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C
404188	PROJECT PHASE I	OR	50	20			46	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	28	46	74	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	28	50	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	23	41	64	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	23	29	52	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	P
404195	PROJECT PHASE II	TW	100	40			95	P	P
404195	PROJECT PHASE II	PR	50	20			45	P	P

GRAND TOTAL = 1082/1500 , RESULT:FIRST CLASS WITH DISTINCTION [\$ 0.1]

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023033 GHOSH SHUBHAM JOGPRAKASH LILIMA ,71526585H ,MITP ,B120023033

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	57	70	P	C
404182	COMPUTER NETWORKS	PP	100	40	22	50	72	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	38	60	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	20	51	71	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	27	48	75	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	29	48	77	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	47	70	P	P
404191D	SOFT COMPUTING	PP	100	40	27	38	65	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	27	39	66	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			47	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	P
404195	PROJECT PHASE II	TW	100	40			88	P	P
404195	PROJECT PHASE II	PR	50	20			44	P	P

GRAND TOTAL = 1125/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023034 GIRI MUGDHA SHARAD SANDHYA ,71633543D ,MITP ,B120023034

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	54	74	P	C
404182	COMPUTER NETWORKS	PP	100	40	27	44	71	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	23	52	75	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	26	54	80	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	27	38	65	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			46	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			41	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	27	49	76	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	30	48	78	P	P
404191D	SOFT COMPUTING	PP	100	40	20	42	62	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	23	37	60	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	P
404195	PROJECT PHASE II	TW	100	40			92	P	P
404195	PROJECT PHASE II	PR	50	20			45	P	P

GRAND TOTAL = 1153/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023035 GOLESAR ANUJ SANJAY LATA ,71526590D ,MITP ,B120023035

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	39	58	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	42	63	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	18	35	53	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	21	52	73	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	13	42	55	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			34	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	22	39	61	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	36	59	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	24	44	68	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	25	10	--	F	F
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	P
404195	PROJECT PHASE II	TW	100	40			91	P	P
404195	PROJECT PHASE II	PR	50	20			46	P	P

GRAND TOTAL = --/1500 , RESULT:FAILS

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023036 GORE UNNATI VIJENDRA LALITA ,71633544B ,MITP ,B120023036

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	55	70	P	C	404189	MOBILE COMMUNICATION	PP	100	40	24	39	63	P	P
404182	COMPUTER NETWORKS	PP	100	40	26	55	81	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	13	44	57	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	47	68	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	25	54	79	P	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	28	64	92	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	21	29	50	P	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	12	46	58	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			45	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P	C	404195	PROJECT PHASE II	TW	100	40			80	P	P
404188	PROJECT PHASE I	OR	50	20			38	P	C	404195	PROJECT PHASE II	PR	50	20			41	P	P

GRAND TOTAL = 1088/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023037 GURAV ASHWINI MANOHAR TARAVATI ,71633545L ,MITP ,B120023037

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	54	70	P	C	404189	MOBILE COMMUNICATION	PP	100	40	23	47	70	P	P
404182	COMPUTER NETWORKS	PP	100	40	21	37	58	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	48	71	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	37	56	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	23	52	75	P	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	21	51	72	P	C	404192D	WIRELESS NETWORKS	PP	100	40	23	50	73	P	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	08	40	48	P		404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			42	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C	404195	PROJECT PHASE II	TW	100	40			92	P	P
404188	PROJECT PHASE I	OR	50	20			38	P	C	404195	PROJECT PHASE II	PR	50	20			43	P	P

GRAND TOTAL = 1081/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023038 INGLE VAISHALI ASHOK GAUKARNA ,71500059E ,MITP ,B120023038

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	08	43	51	P	C	404189	MOBILE COMMUNICATION	PP	100	40	18	32	50	P	P
404182	COMPUTER NETWORKS	PP	100	40	12	32	44	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	13	29	42	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	35	50	P	C	404191D	SOFT COMPUTING	PP	100	40	08	40	48	P	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	17	54	71	P	C	404192D	WIRELESS NETWORKS	PP	100	40	14	39	53	P	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	04	50	54	P		404193	LAB PRACTICE III(MC & BCS)	TW	50	20			34	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			30	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			22	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P		404195	PROJECT PHASE II	TW	100	40			94	P	P
404188	PROJECT PHASE I	OR	50	20			41	P	C	404195	PROJECT PHASE II	PR	50	20			45	P	P

GRAND TOTAL = 910/1500 , RESULT:FIRST CLASS

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023039 ITWARE ABOLI GANESHRAO SUNITA ,71526618H ,MITP ,B120023039

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	57	75	P	C
404182	COMPUTER NETWORKS	PP	100	40	26	46	72	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	49	71	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	28	53	81	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	26	47	73	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			47	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			45	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	28	46	74	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	27	42	69	P	P
404191D	SOFT COMPUTING	PP	100	40	24	40	64	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	20	28	48	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			47	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			41	P	P
404195	PROJECT PHASE II	TW	100	40			95	P	P
404195	PROJECT PHASE II	PR	50	20			45	P	P

GRAND TOTAL = 1153/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023040 JADHAV SAI MADHURAJ PARUL ,71526623D ,MITP ,B120023040

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	24	56	80	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	47	70	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	23	49	72	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	29	55	84	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	27	54	81	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			47	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			46	P	C
404188	PROJECT PHASE I	OR	50	20			47	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	29	48	77	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	47	72	P	P
404191D	SOFT COMPUTING	PP	100	40	26	47	73	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			45	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			45	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			48	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			43	P	P
404195	PROJECT PHASE II	TW	100	40			99	P	P
404195	PROJECT PHASE II	PR	50	20			49	P	P
414464D	INTERNET OF THING	PP	100	40	30	49	79	P	P

GRAND TOTAL = 1245/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023041 JAWALEKAR PRATIK SAINATH RANJANA ,71633546J ,MITP ,B120023041

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	51	67	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	42	58	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	26	49	75	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	28	59	87	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	09	48	57	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C
404188	PROJECT PHASE I	OR	50	20			41	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	20	44	64	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	18	46	64	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	21	47	68	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	21	42	63	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	P
404195	PROJECT PHASE II	TW	100	40			91	P	P
404195	PROJECT PHASE II	PR	50	20			42	P	P

GRAND TOTAL = 1089/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023042 JAYANT SANJAY DHOLE SINDHU ,71526631E ,MITP ,B120023042

## SEM.:1

404181 VLSI DESIGN & TECHNOLOGY PP 100 40 11 43 54 P C  
 404182 COMPUTER NETWORKS PP 100 40 15 40 55 P C  
 404183 MICROWAVE ENGINEERING PP 100 40 14 42 56 P C  
 404184C SOFTWARE DEFINED RADIO PP 100 40 19 51 70 P C  
 404185D ARTIFICIAL INTELLIGENCE PP 100 40 20 43 63 P C  
 404186 LAB PRACTICE I (CN & MWE) TW 50 20 37 P C  
 404186 LAB PRACTICE I (CN & MWE) OR 50 20 35 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) TW 50 20 41 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) PR 50 20 33 P C  
 404188 PROJECT PHASE I OR 50 20 44 P C

## SEM.:2

404189 MOBILE COMMUNICATION PP 100 40 23 40 63 P  
 404190 BROADBAND COMMUNICATION SYS. PP 100 40 20 30 50 P  
 404191C AUDIO VIDEO ENGINEERING PP 100 40 21 41 62 P  
 404192EA ADV. AUTOMOTIVE ELECTRONICS PP 100 40 19 29 48 P  
 404193 LAB PRACTICE III(MC & BCS) TW 50 20 34 P  
 404193 LAB PRACTICE III(MC & BCS) OR 50 20 36 P  
 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 41 P  
 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 42 P  
 404195 PROJECT PHASE II TW 100 40 98 P  
 404195 PROJECT PHASE II PR 50 20 48 P

GRAND TOTAL = 1010/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023043 JOHARAPURKAR SUNIL TANVI SHUCHITA ,71526635H ,MITP ,B120023043

## SEM.:1

404181 VLSI DESIGN & TECHNOLOGY PP 100 40 23 61 84 P C  
 404182 COMPUTER NETWORKS PP 100 40 25 58 83 P C  
 404183 MICROWAVE ENGINEERING PP 100 40 28 49 77 P C  
 404184A DIGITAL IMAGE PROCESSING PP 100 40 30 60 90 P C  
 404185D ARTIFICIAL INTELLIGENCE PP 100 40 26 51 77 P C  
 404186 LAB PRACTICE I (CN & MWE) TW 50 20 46 P C  
 404186 LAB PRACTICE I (CN & MWE) OR 50 20 44 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) TW 50 20 47 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) PR 50 20 42 P C  
 404188 PROJECT PHASE I OR 50 20 42 P C

## SEM.:2

404189 MOBILE COMMUNICATION PP 100 40 30 54 84 P  
 404190 BROADBAND COMMUNICATION SYS. PP 100 40 30 48 78 P  
 404191C AUDIO VIDEO ENGINEERING PP 100 40 28 58 86 P  
 404192EA ADV. AUTOMOTIVE ELECTRONICS PP 100 40 27 43 70 P  
 404193 LAB PRACTICE III(MC & BCS) TW 50 20 46 P  
 404193 LAB PRACTICE III(MC & BCS) OR 50 20 44 P  
 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 46 P  
 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 46 P  
 404195 PROJECT PHASE II TW 100 40 99 P  
 404195 PROJECT PHASE II PR 50 20 49 P

GRAND TOTAL = 1280/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023044 JOSHI CHINMAYA VIJAYKUMAR VANDANA ,71526637D ,MITP ,B120023044

## SEM.:1

404181 VLSI DESIGN & TECHNOLOGY PP 100 40 15 45 60 P C  
 404182 COMPUTER NETWORKS PP 100 40 17 46 63 P C  
 404183 MICROWAVE ENGINEERING PP 100 40 24 38 62 P C  
 404184B EMBEDDED SYSTEMS & RTOS PP 100 40 26 52 78 P C  
 404185D ARTIFICIAL INTELLIGENCE PP 100 40 23 47 70 P C  
 404186 LAB PRACTICE I (CN & MWE) TW 50 20 40 P C  
 404186 LAB PRACTICE I (CN & MWE) OR 50 20 38 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) TW 50 20 43 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) PR 50 20 36 P C  
 404188 PROJECT PHASE I OR 50 20 42 P C

## SEM.:2

404189 MOBILE COMMUNICATION PP 100 40 25 36 61 P  
 404190 BROADBAND COMMUNICATION SYS. PP 100 40 12 52 64 P  
 404191D SOFT COMPUTING PP 100 40 29 44 73 P  
 404192EA ADV. AUTOMOTIVE ELECTRONICS PP 100 40 24 39 63 P  
 404193 LAB PRACTICE III(MC & BCS) TW 50 20 40 P  
 404193 LAB PRACTICE III(MC & BCS) OR 50 20 40 P  
 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 45 P  
 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 39 P  
 404195 PROJECT PHASE II TW 100 40 92 P  
 404195 PROJECT PHASE II PR 50 20 45 P

GRAND TOTAL = 1094/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023045 JOSHI UMANG SANJAY MANJU ,71526644G ,MITP ,B120023045

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	47	60	P	C
404182	COMPUTER NETWORKS	PP	100	40	14	41	55	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	35	57	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	39	56	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	10	43	53	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C
404188	PROJECT PHASE I	OR	50	20			45	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	32	49	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	13	28	41	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	44	59	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	22	30	52	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			35	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	
404195	PROJECT PHASE II	TW	100	40			97	P	
404195	PROJECT PHASE II	PR	50	20			47	P	

GRAND TOTAL = 971/1500 , RESULT:FIRST CLASS [\$ 0.1]

B120023046 JOTWANI SONAL GOVIND USHA ,71526645E ,MITP ,B120023046

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	48	68	P	C
404182	COMPUTER NETWORKS	PP	100	40	24	50	74	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	56	75	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	28	60	88	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	23	46	69	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C
404188	PROJECT PHASE I	OR	50	20			39	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	25	42	67	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	29	48	77	P	
404191D	SOFT COMPUTING	PP	100	40	23	38	61	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	24	31	55	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			44	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			44	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			34	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	
404195	PROJECT PHASE II	TW	100	40			90	P	
404195	PROJECT PHASE II	PR	50	20			43	P	

GRAND TOTAL = 1127/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023047 JUVEKAR SHAUNAK PARAG ANUPAMA ,71526648K ,MITP ,B120023047

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	40	55	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	45	62	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	15	39	54	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	26	49	75	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	20	47	67	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			37	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			46	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	21	44	65	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	27	38	65	P	
404191D	SOFT COMPUTING	PP	100	40	25	41	66	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			42	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			41	P	
404195	PROJECT PHASE II	TW	100	40			96	P	
404195	PROJECT PHASE II	PR	50	20			47	P	
414464D	INTERNET OF THING	PP	100	40	27	40	67	P	

GRAND TOTAL = 1084/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023048 KADAM PRANAV ANIRUDDHA SWATI ,71526650M ,MITP ,B120023048

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	34	50	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	35	52	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	38	59	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	26	51	77	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	26	37	63	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			45	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			47	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			47	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	26	42	68	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	28	38	66	P	
404191D	SOFT COMPUTING	PP	100	40	26	43	69	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			46	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			48	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			44	P	
404195	PROJECT PHASE II	TW	100	40			99	P	
404195	PROJECT PHASE II	PR	50	20			49	P	
414464D	INTERNET OF THING	PP	100	40	25	35	60	P	

GRAND TOTAL = 1118/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023049 KAJARIA HRUSHIKA CHETAN SWATI ,71526652H ,MITP ,B120023049

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	37	51	P	C
404182	COMPUTER NETWORKS	PP	100	40	15	40	55	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	12	32	44	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	17	41	58	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	17	40	57	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	25	38	63	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	16	30	46	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	21	49	70	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			42	P	
404195	PROJECT PHASE II	TW	100	40			91	P	
404195	PROJECT PHASE II	PR	50	20			46	P	
414464D	INTERNET OF THING	PP	100	40	25	40	65	P	

GRAND TOTAL = 999/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023050 KALE SARVESH SHIRISH SAVITA ,71526656L ,MITP ,B120023050

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	53	69	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	42	58	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	38	59	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	24	44	68	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	12	36	48	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C
404188	PROJECT PHASE I	OR	50	20			46	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	27	39	66	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	33	53	P	
404191D	SOFT COMPUTING	PP	100	40	22	34	56	P	
404192D	WIRELESS NETWORKS	PP	100	40	14	31	45	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			36	P	
404195	PROJECT PHASE II	TW	100	40			98	P	
404195	PROJECT PHASE II	PR	50	20			48	P	

GRAND TOTAL = 1025/1500 , RESULT:FIRST CLASS WITH DISTINCTION



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023051 KALJE MANJIRI AJAY ROHINI ,71633547G ,MITP ,B120023051

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	49	69	P	C
404182	COMPUTER NETWORKS	PP	100	40	20	45	65	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	47	68	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	30	64	94	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	19	42	61	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			44	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	55	73	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	29	47	76	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	20	52	72	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	23	39	62	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			45	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			44	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			43	P	P
404195	PROJECT PHASE II	TW	100	40			95	P	P
404195	PROJECT PHASE II	PR	50	20			45	P	P

GRAND TOTAL = 1165/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023052 KAMBLE NEHA ASHOK VIDYA ,71633548E ,MITP ,B120023052

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	54	64	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	49	66	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	43	65	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	30	60	90	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	26	51	77	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			31	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	23	46	69	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	39	62	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	27	54	81	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	22	36	58	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			32	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	P
404195	PROJECT PHASE II	TW	100	40			95	P	P
404195	PROJECT PHASE II	PR	50	20			46	P	P

GRAND TOTAL = 1122/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023053 KAMBLE PRIYANKA VASANTRAO NANDA ,71400284E ,MITP ,B120023053

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	45	55	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	33	49	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	14	45	59	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	21	54	75	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	08	53	61	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			46	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			47	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	24	48	72	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	38	58	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	14	38	52	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	11	31	42	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			45	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			41	P	P
404195	PROJECT PHASE II	TW	100	40			96	P	P
404195	PROJECT PHASE II	PR	50	20			42	P	P

GRAND TOTAL = 1040/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023054 KAREMORE ARYA PANJAB KAVERI ,71526669B ,MITP ,B120023054

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	44	62	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	48	69	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	20	46	66	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	20	58	78	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	25	55	80	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	20	50	70	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	35	61	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	51	69	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	20	31	51	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	P
404195	PROJECT PHASE II	TW	100	40			90	P	P
404195	PROJECT PHASE II	PR	50	20			41	P	P

GRAND TOTAL = 1075/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023055 KARKHANIS MINOTI SANJAY AMITA ,71633549C ,MITP ,B120023055

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	50	63	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	41	57	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	28	40	68	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	18	62	80	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	16	44	60	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	19	35	54	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	41	65	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	54	69	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	17	19	--	F	F
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			45	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			44	P	P
404195	PROJECT PHASE II	TW	100	40			91	P	P
404195	PROJECT PHASE II	PR	50	20			42	P	P

GRAND TOTAL = --/1500 , RESULT:FAILS

B120023056 KAVITA BADRILAL CHOUDHARI SONIDEVI ,71526677C ,MITP ,B120023056

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	39	56	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	40	57	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	36	57	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	28	44	72	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	24	39	63	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			47	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			45	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	28	32	60	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	36	61	P	P
404191D	SOFT COMPUTING	PP	100	40	17	41	58	P	P
404192D	WIRELESS NETWORKS	PP	100	40	09	33	42	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	P
404195	PROJECT PHASE II	TW	100	40			95	P	P
404195	PROJECT PHASE II	PR	50	20			45	P	P

GRAND TOTAL = 1043/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023057 KELKAR NEEL DHANANJAY SWATI ,71526680C ,MITP ,B120023057

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	34	49	P	C	404189	MOBILE COMMUNICATION	PP	100	40	24	39	63	P	P
404182	COMPUTER NETWORKS	PP	100	40	19	37	56	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	19	41	60	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	39	54	P	C	404191D	SOFT COMPUTING	PP	100	40	18	37	55	P	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	26	34	60	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	20	36	56	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			35	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P	C	404195	PROJECT PHASE II	TW	100	40			95	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C	404195	PROJECT PHASE II	PR	50	20			48	P	P
404188	PROJECT PHASE I	OR	50	20			45	P	C	414464D	INTERNET OF THING	PP	100	40	25	33	58	P	P

GRAND TOTAL = 1013/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023058 KHADE MANALI SHANKAR CHANDA ,71633550G ,MITP ,B120023058

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	51	70	P	C	404189	MOBILE COMMUNICATION	PP	100	40	27	51	78	P	P
404182	COMPUTER NETWORKS	PP	100	40	25	46	71	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	46	66	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	20	44	64	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	22	56	78	P	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	19	55	74	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	21	48	69	P	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	15	46	61	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			46	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			47	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			43	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C	404195	PROJECT PHASE II	TW	100	40			78	P	P
404188	PROJECT PHASE I	OR	50	20			36	P	C	404195	PROJECT PHASE II	PR	50	20			40	P	P

GRAND TOTAL = 1110/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023059 KHUJAT SOURABH SADASHIV SNEHA ,71526695M ,MITP ,B120023059

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	38	58	P	C	404189	MOBILE COMMUNICATION	PP	100	40	22	46	68	P	P
404182	COMPUTER NETWORKS	PP	100	40	19	46	65	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	37	57	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	41	62	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	17	39	56	P	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	18	50	68	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	13	33	46	P	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	25	43	68	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			33	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			42	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C	404195	PROJECT PHASE II	TW	100	40			98	P	P
404188	PROJECT PHASE I	OR	50	20			44	P	C	404195	PROJECT PHASE II	PR	50	20			48	P	P

GRAND TOTAL = 1043/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023060 KOTHARI ROHIT RAJENDRA LATA ,71526704D ,MITP ,B120023060

## SEM.:1

404181 VLSI DESIGN & TECHNOLOGY PP 100 40 21 51 72 P C  
 404182 COMPUTER NETWORKS PP 100 40 19 47 66 P C  
 404183 MICROWAVE ENGINEERING PP 100 40 24 45 69 P C  
 404184A DIGITAL IMAGE PROCESSING PP 100 40 28 47 75 P C  
 404185B ELECTRONIC PRODUCT DESIGN PP 100 40 16 40 56 P C  
 404186 LAB PRACTICE I (CN & MWE) TW 50 20 38 P C  
 404186 LAB PRACTICE I (CN & MWE) OR 50 20 38 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) TW 50 20 43 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) PR 50 20 36 P C  
 404188 PROJECT PHASE I OR 50 20 47 P C

## SEM.:2

404189 MOBILE COMMUNICATION PP 100 40 24 45 69 P  
 404190 BROADBAND COMMUNICATION SYS. PP 100 40 24 37 61 P  
 404191D SOFT COMPUTING PP 100 40 21 38 59 P  
 404192EA ADV. AUTOMOTIVE ELECTRONICS PP 100 40 12 35 47 P  
 404193 LAB PRACTICE III(MC & BCS) TW 50 20 40 P  
 404193 LAB PRACTICE III(MC & BCS) OR 50 20 40 P  
 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 44 P  
 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 41 P  
 404195 PROJECT PHASE II TW 100 40 98 P  
 404195 PROJECT PHASE II PR 50 20 49 P

GRAND TOTAL = 1088/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023061 KSHIRSAGAR ANEESH LALITKUMAR ANJALI ,71526705B ,MITP ,B120023061

## SEM.:1

404181 VLSI DESIGN & TECHNOLOGY PP 100 40 18 41 59 P C  
 404182 COMPUTER NETWORKS PP 100 40 21 43 64 P C  
 404183 MICROWAVE ENGINEERING PP 100 40 25 30 55 P C  
 404184A DIGITAL IMAGE PROCESSING PP 100 40 25 49 74 P C  
 404185D ARTIFICIAL INTELLIGENCE PP 100 40 25 46 71 P C  
 404186 LAB PRACTICE I (CN & MWE) TW 50 20 44 P C  
 404186 LAB PRACTICE I (CN & MWE) OR 50 20 44 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) TW 50 20 47 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) PR 50 20 46 P C  
 404188 PROJECT PHASE I OR 50 20 45 P C

## SEM.:2

404189 MOBILE COMMUNICATION PP 100 40 24 39 63 P  
 404190 BROADBAND COMMUNICATION SYS. PP 100 40 22 42 64 P  
 404191D SOFT COMPUTING PP 100 40 18 39 57 P  
 404193 LAB PRACTICE III(MC & BCS) TW 50 20 45 P  
 404193 LAB PRACTICE III(MC & BCS) OR 50 20 44 P  
 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 43 P  
 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 42 P  
 404195 PROJECT PHASE II TW 100 40 97 P  
 404195 PROJECT PHASE II PR 50 20 47 P  
 414464D INTERNET OF THING PP 100 40 27 45 72 P

GRAND TOTAL = 1123/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023062 KULKARNI ANIRUDH PRAVIN PRAJOKATI ,71526710J ,MITP ,B120023062

## SEM.:1

404181 VLSI DESIGN & TECHNOLOGY PP 100 40 21 50 71 P C  
 404182 COMPUTER NETWORKS PP 100 40 21 45 66 P C  
 404183 MICROWAVE ENGINEERING PP 100 40 26 48 74 P C  
 404184A DIGITAL IMAGE PROCESSING PP 100 40 30 56 86 P C  
 404185D ARTIFICIAL INTELLIGENCE PP 100 40 23 44 67 P C  
 404186 LAB PRACTICE I (CN & MWE) TW 50 20 46 P C  
 404186 LAB PRACTICE I (CN & MWE) OR 50 20 44 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) TW 50 20 47 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) PR 50 20 46 P C  
 404188 PROJECT PHASE I OR 50 20 47 P C

## SEM.:2

404189 MOBILE COMMUNICATION PP 100 40 27 54 81 P  
 404190 BROADBAND COMMUNICATION SYS. PP 100 40 26 46 72 P  
 404191D SOFT COMPUTING PP 100 40 28 43 71 P  
 404193 LAB PRACTICE III(MC & BCS) TW 50 20 45 P  
 404193 LAB PRACTICE III(MC & BCS) OR 50 20 46 P  
 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 48 P  
 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 45 P  
 404195 PROJECT PHASE II TW 100 40 99 P  
 404195 PROJECT PHASE II PR 50 20 49 P  
 414464D INTERNET OF THING PP 100 40 29 51 80 P

GRAND TOTAL = 1230/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023063 KULKARNI SHREYAS CHANDRASHEKHAR VARSHA ,71526713C ,MITP ,B120023063

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	48	64	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	44	67	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	18	50	68	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	21	58	79	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	26	41	67	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			43	P	C
404188	PROJECT PHASE I	OR	50	20			46	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	23	37	60	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	16	52	68	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	20	43	63	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	27	48	75	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			44	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			44	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			47	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			46	P	P
404195	PROJECT PHASE II	TW	100	40			98	P	P
404195	PROJECT PHASE II	PR	50	20			48	P	P

GRAND TOTAL = 1155/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023064 KUMAR SOUMYA ARVIND MANISHA ,71526715K ,MITP ,B120023064

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	42	58	P	C
404182	COMPUTER NETWORKS	PP	100	40	22	45	67	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	25	45	70	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	19	50	69	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	11	53	64	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			31	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	23	50	73	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	18	49	67	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	28	48	76	P	P
404192D	WIRELESS NETWORKS	PP	100	40	22	49	71	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			41	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			46	P	P
404195	PROJECT PHASE II	TW	100	40			96	P	P
404195	PROJECT PHASE II	PR	50	20			45	P	P

GRAND TOTAL = 1122/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023065 LADNIYA BHUSHAN BHARAT BHARATI ,71526725G ,MITP ,B120023065

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	47	69	P	C
404182	COMPUTER NETWORKS	PP	100	40	20	46	66	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	43	62	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	30	50	80	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	23	55	78	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			46	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			48	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	21	59	80	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	50	75	P	P
404191D	SOFT COMPUTING	PP	100	40	27	41	68	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			43	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			47	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			47	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	P
404195	PROJECT PHASE II	TW	100	40			98	P	P
404195	PROJECT PHASE II	PR	50	20			49	P	P
414464D	INTERNET OF THING	PP	100	40	25	42	67	P	P

GRAND TOTAL = 1184/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023067 LAKADE SOHAM SAURABH SONALI ,71526727C ,MITP ,B120023067

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	32	47	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	33	50	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	28	50	P	
404184C	SOFTWARE DEFINED RADIO	PP	100	40	20	50	70	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	14	48	62	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C
404188	PROJECT PHASE I	OR	50	20			41	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	26	45	71	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	41	65	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	25	42	67	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	22	28	50	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			43	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			44	P	

GRAND TOTAL = 1030/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023068 LONDHE PRADNYA PRASHANT NIRMALA ,71526735D ,MITP ,B120023068

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	38	51	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	35	52	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	17	32	49	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	29	62	91	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	23	40	63	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			25	P	C
404188	PROJECT PHASE I	OR	50	20			45	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	22	41	63	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	17	47	64	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	53	72	P	
404192D	WIRELESS NETWORKS	PP	100	40	20	42	62	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			44	P	
404195	PROJECT PHASE II	TW	100	40			98	P	
404195	PROJECT PHASE II	PR	50	20			48	P	

GRAND TOTAL = 1065/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023069 LOVELEEN JAYANT CHAUDHARI NITA ,71526736B ,MITP ,B120023069

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	41	54	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	47	63	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	18	30	48	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	24	40	64	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	18	38	56	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	19	40	59	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	43	67	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	12	32	44	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	17	33	50	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			43	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			96	P	
404195	PROJECT PHASE II	PR	50	20			44	P	

GRAND TOTAL = 995/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023070 MAHANGARE ASHWINI SHIVAJI SULOCHANA ,71633551E ,MITP ,B120023070

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	37	54	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	39	55	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	17	42	59	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	21	56	77	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	15	47	62	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C
404188	PROJECT PHASE I	OR	50	20			34	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	34	52	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	55	79	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	51	69	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	16	39	55	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			42	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			42	P	
404195	PROJECT PHASE II	TW	100	40			90	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 1044/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023071 MARATHE SHYAMAL NIVRUTTINATH SUNITA ,71633552C ,MITP ,B120023071

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	25	52	77	P	C
404182	COMPUTER NETWORKS	PP	100	40	15	48	63	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	28	44	72	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	24	57	81	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	12	39	51	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	21	43	64	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	54	74	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	52	70	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	20	39	59	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			45	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			45	P	
404195	PROJECT PHASE II	TW	100	40			84	P	
404195	PROJECT PHASE II	PR	50	20			43	P	

GRAND TOTAL = 1110/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023072 MARKAD URVISH SANDEEP VANDANA SANDEEP MARKAD ,71526753B ,MITP ,B120023072

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	46	63	P	C
404182	COMPUTER NETWORKS	PP	100	40	10	41	51	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	24	42	66	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	26	52	78	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	15	47	62	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			44	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	22	34	56	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	42	64	P	
404191D	SOFT COMPUTING	PP	100	40	17	38	55	P	
404192D	WIRELESS NETWORKS	PP	100	40	16	36	52	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			97	P	
404195	PROJECT PHASE II	PR	50	20			48	P	

GRAND TOTAL = 1058/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023073 MITRE ANOSH VISHWAJEET PALLAVI ,71633553M ,MITP ,B120023073

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	07	34	41	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	38	55	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	36	55	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	20	38	58	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	20	33	53	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			41	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	21	44	65	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	19	42	61	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	26	57	83	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	20	33	53	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			43	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			44	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			46	P	
404195	PROJECT PHASE II	TW	100	40			90	P	
404195	PROJECT PHASE II	PR	50	20			45	P	

GRAND TOTAL = 1038/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023074 MOHAMMAD AKBAR MOHAMMAD GULAM DASTGEER KHATIZA ,71526758C ,MITP ,B120023074

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	42	56	P	C
404182	COMPUTER NETWORKS	PP	100	40	15	42	57	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	15	29	44	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	24	42	66	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	16	35	51	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	21	43	64	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	21	51	72	P	
404191D	SOFT COMPUTING	PP	100	40	16	36	52	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	20	36	56	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			37	P	
404195	PROJECT PHASE II	TW	100	40			92	P	
404195	PROJECT PHASE II	PR	50	20			44	P	

GRAND TOTAL = 997/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023075 MOHAMMED ADNAN ATIQR RAHMAN SHAIKH SHAIKH SHABNAM ATIQ ,71300387B ,MITP ,B120023075

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	07	35	42	P	C
404182	COMPUTER NETWORKS	PP	100	40	19	49	68	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	10	34	44	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	19	58	77	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	19	32	51	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			34	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			22	P	C
404188	PROJECT PHASE I	OR	50	20			36	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	19	38	57	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	17	50	67	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	22	42	64	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			35	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			35	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404195	PROJECT PHASE II	TW	100	40			75	P	
404195	PROJECT PHASE II	PR	50	20			35	P	
414464D	INTERNET OF THING	PP	100	40	25	55	80	P	

GRAND TOTAL = 969/1500 , RESULT:FIRST CLASS



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023076 MOHIT GANJOO DOLLY ,71526766D ,MITP ,B120023076

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	48	60	P	C
404182	COMPUTER NETWORKS	PP	100	40	15	40	55	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	18	45	63	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	16	60	76	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	12	30	42	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	20	50	70	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	51	76	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	20	34	54	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	24	43	67	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			41	P	P
404195	PROJECT PHASE II	TW	100	40			97	P	P
404195	PROJECT PHASE II	PR	50	20			45	P	P

GRAND TOTAL = 1050/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023077 MOKAL SONAL PUNDLIK JAYSHREE ,71633554K ,MITP ,B120023077

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	24	47	71	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	48	64	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	27	45	72	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	24	66	90	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	16	43	59	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	25	40	65	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	60	84	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	25	57	82	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	22	37	59	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			42	P	P
404195	PROJECT PHASE II	TW	100	40			92	P	P
404195	PROJECT PHASE II	PR	50	20			41	P	P

GRAND TOTAL = 1140/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023078 MOPKAR ASHVINI GAJANAN NANDA ,71633555H ,MITP ,B120023078

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	48	65	P	C
404182	COMPUTER NETWORKS	PP	100	40	20	46	66	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	27	58	85	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	23	58	81	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	08	49	57	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	20	46	66	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	21	52	73	P	P
404191D	SOFT COMPUTING	PP	100	40	25	38	63	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	16	32	48	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	P
404195	PROJECT PHASE II	TW	100	40			90	P	P
404195	PROJECT PHASE II	PR	50	20			41	P	P

GRAND TOTAL = 1086/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023079 MUNI YASH HIMANSHU MANISHA ,71400388D ,MITP ,B120023079

SEM.:1										SEM.:2								
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	39	49	P	C	404189	MOBILE COMMUNICATION	PP	100	40	23	33	56	P
404182	COMPUTER NETWORKS	PP	100	40	10	32	42	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	21	45	66	P
404183	MICROWAVE ENGINEERING	PP	100	40	12	31	43	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	17	40	57	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	18	47	65	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			34	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	12	29	41	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			33	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			34	P	C	404195	PROJECT PHASE II	TW	100	40			95	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			28	P	C	404195	PROJECT PHASE II	PR	50	20			47	P
404188	PROJECT PHASE I	OR	50	20			41	P	C	414464D	INTERNET OF THING	PP	100	40	23	52	75	P

GRAND TOTAL = 954/1500 , RESULT:FIRST CLASS

B120023080 MUSTAFA MAL ALFIYA ,71400392B ,MITP ,B120023080

SEM.:1										SEM.:2								
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	39	50	P	C	404189	MOBILE COMMUNICATION	PP	100	40	20	32	52	P
404182	COMPUTER NETWORKS	PP	100	40	12	31	43	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	18	45	63	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	18	--	F		404191D	SOFT COMPUTING	PP	100	40	17	32	49	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	20	46	66	P	C	404192D	WIRELESS NETWORKS	PP	100	40	18	17	--	F
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	13	28	41	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			35	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			25	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P		404195	PROJECT PHASE II	TW	100	40			75	P
404188	PROJECT PHASE I	OR	50	20			37	P	C	404195	PROJECT PHASE II	PR	50	20			35	P

GRAND TOTAL = --/1500 , RESULT:FAILS

RESULT RESERVED FOR BACKLOGS.

B120023081 NALANDE SHRUTI MAHENDRA CHHAYA ,71526785L ,MITP ,B120023081

SEM.:1										SEM.:2								
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	52	66	P	C	404189	MOBILE COMMUNICATION	PP	100	40	16	39	55	P
404182	COMPUTER NETWORKS	PP	100	40	24	41	65	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	49	71	P
404183	MICROWAVE ENGINEERING	PP	100	40	27	37	64	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	17	54	71	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	27	40	67	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	19	30	49	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	11	32	43	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			45	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C	404195	PROJECT PHASE II	TW	100	40			98	P
404188	PROJECT PHASE I	OR	50	20			46	P	C	404195	PROJECT PHASE II	PR	50	20			48	P

GRAND TOTAL = 1064/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023082 NESARGI RITVIK SUNEEL BHAVANA ,71400416C ,MITP ,B120023082

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	08	44	52	P	C
404182	COMPUTER NETWORKS	PP	100	40	15	44	59	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	33	54	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	20	51	71	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	22	42	64	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			31	P	C
404188	PROJECT PHASE I	OR	50	20			44	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	24	29	53	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	18	48	66	P	
404191D	SOFT COMPUTING	PP	100	40	19	42	61	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	
404195	PROJECT PHASE II	TW	100	40			98	P	
404195	PROJECT PHASE II	PR	50	20			48	P	
414464D	INTERNET OF THING	PP	100	40	27	49	76	P	

GRAND TOTAL = 1059/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023083 NIGAM NIKHIL RAJESH NEESHA ,71526798B ,MITP ,B120023083

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	41	54	P	C
404182	COMPUTER NETWORKS	PP	100	40	12	32	44	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	13	32	45	P	
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	27	38	65	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	19	41	60	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			45	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	18	42	60	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	17	45	62	P	
404191D	SOFT COMPUTING	PP	100	40	15	39	54	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			98	P	
404195	PROJECT PHASE II	PR	50	20			48	P	
414464D	INTERNET OF THING	PP	100	40	25	43	68	P	

GRAND TOTAL = 1011/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023084 NIKAM ANIMESH DHANANJAY MADHURI ,71526800H ,MITP ,B120023084

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	49	62	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	51	74	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	16	35	51	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	28	49	77	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	24	49	73	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	24	32	56	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	14	50	64	P	
404191D	SOFT COMPUTING	PP	100	40	16	43	59	P	
404192D	WIRELESS NETWORKS	PP	100	40	16	40	56	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			39	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	
404195	PROJECT PHASE II	TW	100	40			86	P	
404195	PROJECT PHASE II	PR	50	20			43	P	

GRAND TOTAL = 1049/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023085 NIKAM KUNJARI CHHABAN SANJIVANI ,71526801F ,MITP ,B120023085

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	56	70	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	53	76	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	24	50	74	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	30	67	97	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	17	41	58	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	20	49	69	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	16	57	73	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	25	56	81	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	20	45	65	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			42	P	
404195	PROJECT PHASE II	TW	100	40			90	P	
404195	PROJECT PHASE II	PR	50	20			40	P	

GRAND TOTAL = 1148/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023086 NIKITA NAWANDAR RAJSHRI ,71526803B ,MITP ,B120023086

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	50	67	P	C
404182	COMPUTER NETWORKS	PP	100	40	19	47	66	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	27	49	76	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	25	56	81	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	10	49	59	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	15	43	58	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	52	78	P	
404191D	SOFT COMPUTING	PP	100	40	22	42	64	P	
404192D	WIRELESS NETWORKS	PP	100	40	22	44	66	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			42	P	
404195	PROJECT PHASE II	TW	100	40			96	P	
404195	PROJECT PHASE II	PR	50	20			46	P	

GRAND TOTAL = 1120/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023087 OMKAR ASHOK KALANGE SHARMILA ,71526809M ,MITP ,B120023087

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	26	44	70	P	C
404182	COMPUTER NETWORKS	PP	100	40	22	54	76	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	23	47	70	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	27	49	76	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	22	59	81	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C
404188	PROJECT PHASE I	OR	50	20			47	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	24	53	77	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	57	83	P	
404191D	SOFT COMPUTING	PP	100	40	20	37	57	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			41	P	
404195	PROJECT PHASE II	TW	100	40			98	P	
404195	PROJECT PHASE II	PR	50	20			49	P	
414464D	INTERNET OF THING	PP	100	40	17	49	66	P	

GRAND TOTAL = 1160/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023088 PAARTH PRADEEP HEBBALKAR RAJANI ,71526810E ,MITP ,B120023088

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	42	58	P	C
404182	COMPUTER NETWORKS	PP	100	40	19	41	60	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	23	38	61	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	26	47	73	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	22	48	70	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P	C
404188	PROJECT PHASE I	OR	50	20			44	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	23	36	59	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	55	80	P	
404191D	SOFT COMPUTING	PP	100	40	21	44	65	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	22	40	62	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			41	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			43	P	
404195	PROJECT PHASE II	TW	100	40			94	P	
404195	PROJECT PHASE II	PR	50	20			45	P	

GRAND TOTAL = 1080/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023089 PALKAR ROHAN JAYANT SNEHA ,71526813K ,MITP ,B120023089

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	45	63	P	C
404182	COMPUTER NETWORKS	PP	100	40	18	49	67	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	36	58	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	27	53	80	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	24	47	71	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			46	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			46	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			46	P	C
404188	PROJECT PHASE I	OR	50	20			48	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	37	54	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	54	78	P	
404191D	SOFT COMPUTING	PP	100	40	25	42	67	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	22	38	60	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			44	P	
404195	PROJECT PHASE II	TW	100	40			98	P	
404195	PROJECT PHASE II	PR	50	20			47	P	

GRAND TOTAL = 1137/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023090 PARANJPYE DHURUV ARVIND SMITA ,71526819J ,MITP ,B120023090

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	48	62	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	46	67	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	27	45	72	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	29	46	75	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	24	53	77	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			43	P	C
404188	PROJECT PHASE I	OR	50	20			48	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	25	49	74	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	52	77	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	28	55	83	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	25	42	67	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			41	P	
404195	PROJECT PHASE II	TW	100	40			99	P	
404195	PROJECT PHASE II	PR	50	20			49	P	

GRAND TOTAL = 1188/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023091 PARLIKAR ANIKET VENKATESH SUCHETA ,71526823G ,MITP ,B120023091

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	18	33	51	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	37	53	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	25	41	66	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	27	48	75	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	19	46	65	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	19	40	59	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	18	52	70	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	22	41	63	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	21	34	55	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			44	P	

GRAND TOTAL = 1036/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023092 PARVEZ M. KHAN ALIMUNNISA ,71526824E ,MITP ,B120023092

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	36	50	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	43	64	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	24	44	68	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	28	45	73	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	18	45	63	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			43	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	20	44	64	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	21	56	77	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	23	55	78	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	23	42	65	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			43	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			44	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			42	P	
404195	PROJECT PHASE II	TW	100	40			98	P	
404195	PROJECT PHASE II	PR	50	20			48	P	

GRAND TOTAL = 1114/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023093 PATEL SIDDHI BHARAT RITA ,71526830K ,MITP ,B120023093

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	29	49	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	54	77	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	27	40	67	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	29	52	81	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	17	41	58	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P	C
404188	PROJECT PHASE I	OR	50	20			45	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	26	43	69	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	48	70	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	25	57	82	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	22	39	61	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			41	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			42	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			45	P	

GRAND TOTAL = 1125/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023094 PATIL AKSHAY CHANDRASHEKHAR NAYANA ,71526832F ,MITP ,B120023094

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	30	41	P	C
404182	COMPUTER NETWORKS	PP	100	40	18	37	55	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	20	32	52	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	15	42	57	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	10	35	45	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C
404188	PROJECT PHASE I	OR	50	20			46	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	21	33	54	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	17	47	64	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	16	47	63	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	17	30	47	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			94	P	
404195	PROJECT PHASE II	PR	50	20			47	P	

GRAND TOTAL = 959/1500 , RESULT:FIRST CLASS

B120023096 PATIL SHUBHANGI RAJKUMAR MAHANANDA ,71100480D ,MITP ,B120023096

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	32	43	P	
404182	COMPUTER NETWORKS	PP	100	40	13	19	--	F	
404183	MICROWAVE ENGINEERING	PP	100	40	14	30	44	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	16	39	55	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	08	44	52	P	
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			34	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			22	P	C
404188	PROJECT PHASE I	OR	50	20			43	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	13	28	41	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	12	29	41	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	20	36	56	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	03	17	--	F	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	
404195	PROJECT PHASE II	TW	100	40			94	P	
404195	PROJECT PHASE II	PR	50	20			46	P	

GRAND TOTAL = --/1500 , RESULT:FAILS

RESULT RESERVED FOR BACKLOGS.

B120023097 PATIL SIDDHIKA RAMDAS RASIKA ,71526845H ,MITP ,B120023097

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	40	57	P	C
404182	COMPUTER NETWORKS	PP	100	40	24	64	88	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	25	59	84	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	27	65	92	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	25	44	69	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			45	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	19	49	68	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	54	79	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	27	55	82	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			43	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			42	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			45	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			46	P	
404195	PROJECT PHASE II	TW	100	40			98	P	
404195	PROJECT PHASE II	PR	50	20			48	P	
414464D	INTERNET OF THING	PP	100	40	29	55	84	P	

GRAND TOTAL = 1239/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023098 PATWEKAR SHOAB TASADDUKHAHMED SHAHANAJ ,71633556F ,MITP ,B120023098

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	08	33	41	P	C
404182	COMPUTER NETWORKS	PP	100	40	19	56	75	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	13	48	61	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	23	54	77	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	19	49	68	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			46	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	19	48	67	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	18	54	72	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	20	47	67	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	22	48	70	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			44	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			41	P	P
404195	PROJECT PHASE II	TW	100	40			97	P	P
404195	PROJECT PHASE II	PR	50	20			46	P	P

GRAND TOTAL = 1111/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023099 PILLAI HRISHIKESH MANU USHA ,71526855E ,MITP ,B120023099

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	40	53	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	45	62	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	25	42	67	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	28	32	60	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	21	35	56	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P	C
404188	PROJECT PHASE I	OR	50	20			44	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	21	40	61	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	28	48	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	22	37	59	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	18	29	47	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			33	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			45	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			42	P	P
404195	PROJECT PHASE II	TW	100	40			95	P	P
404195	PROJECT PHASE II	PR	50	20			45	P	P

GRAND TOTAL = 1011/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023100 POOJA SATISH LOKHANDE PRAGATI ,71526858K ,MITP ,B120023100

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	28	42	P	C
404182	COMPUTER NETWORKS	PP	100	40	19	46	65	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	25	45	70	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	15	43	58	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	06	50	56	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	14	50	64	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	43	68	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	17	51	68	P	P
404192D	WIRELESS NETWORKS	PP	100	40	07	47	54	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			43	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	P
404195	PROJECT PHASE II	TW	100	40			95	P	P
404195	PROJECT PHASE II	PR	50	20			45	P	P

GRAND TOTAL = 1032/1500 , RESULT:FIRST CLASS WITH DISTINCTION



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023101 PRASAD GOVIND KHAKE JAYSHREE ,71526864D ,MITP ,B120023101

SEM.:1								SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	33	45	P	404189	MOBILE COMMUNICATION	PP	100	40	13	34	47	P
404182	COMPUTER NETWORKS	PP	100	40	15	29	44	P C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	13	47	60	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	29	44	P C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	21	42	63	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	19	50	69	P C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	14	28	42	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	18	36	54	P C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			41	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			35	P C	404195	PROJECT PHASE II	TW	100	40			98	P
404188	PROJECT PHASE I	OR	50	20			45	P C	404195	PROJECT PHASE II	PR	50	20			48	P

GRAND TOTAL = 969/1500 , RESULT:FIRST CLASS

B120023102 RAJAT RAINA SUNITA ,71526881D ,MITP ,B120023102

SEM.:1								SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	38	52	P C	404189	MOBILE COMMUNICATION	PP	100	40	26	47	73	P
404182	COMPUTER NETWORKS	PP	100	40	17	48	65	P C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	54	80	P
404183	MICROWAVE ENGINEERING	PP	100	40	22	42	64	P C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	47	66	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	25	62	87	P C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	21	28	49	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	19	46	65	P C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P C	404195	PROJECT PHASE II	TW	100	40			95	P
404188	PROJECT PHASE I	OR	50	20			43	P C	404195	PROJECT PHASE II	PR	50	20			45	P

GRAND TOTAL = 1092/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023103 RAJWADE ROHIT RAVINDRA LEENA ,71526888M ,MITP ,B120023103

SEM.:1								SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	09	35	44	P C	404189	MOBILE COMMUNICATION	PP	100	40	22	35	57	P
404182	COMPUTER NETWORKS	PP	100	40	21	37	58	P C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	52	77	P
404183	MICROWAVE ENGINEERING	PP	100	40	23	42	65	P C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	20	48	68	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	27	52	79	P C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	23	43	66	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	16	41	57	P C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			41	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			46	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			44	P C	404195	PROJECT PHASE II	TW	100	40			98	P
404188	PROJECT PHASE I	OR	50	20			46	P C	404195	PROJECT PHASE II	PR	50	20			48	P

GRAND TOTAL = 1103/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023104 RAKHI DOMAJI MESHAM MEGHA ,71633558B ,MITP ,B120023104

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	09	37	46	P
404182	COMPUTER NETWORKS	PP	100	40	13	34	47	P C
404183	MICROWAVE ENGINEERING	PP	100	40	17	35	52	P C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	13	29	42	P C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	04	40	44	P C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P C
404188	PROJECT PHASE I	OR	50	20			41	P C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	12	31	43	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	18	40	58	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	17	36	53	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	09	31	40	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			44	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			43	P
404195	PROJECT PHASE II	TW	100	40			95	P
404195	PROJECT PHASE II	PR	50	20			46	P

GRAND TOTAL = 913/1500 , RESULT:FIRST CLASS

RESULT RESERVED FOR BACKLOGS.

B120023105 RAMESHWAR HARSHIT RAMESHWAR MEENAKSHI ,71410841D ,MITP ,B120023105

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	AA	41	41	P
404182	COMPUTER NETWORKS	PP	100	40	12	29	41	P
404183	MICROWAVE ENGINEERING	PP	100	40	24	28	52	P C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	15	42	57	P C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	02	39	41	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			38	P C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			34	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			21	P C
404188	PROJECT PHASE I	OR	50	20			38	P C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	22	34	56	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	18	39	57	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	21	44	65	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	13	23#	36#	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P
404195	PROJECT PHASE II	TW	100	40			78	P
404195	PROJECT PHASE II	PR	50	20			41	P

GRAND TOTAL = 885/1500 , RESULT:HIGHER SECOND CLASS [# 0.4]

RESULT RESERVED FOR BACKLOGS.

B120023106 RATHI PUSHPAK PRAMOD UJJWALA ,71526892K ,MITP ,B120023106

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	09	32	41	P C
404182	COMPUTER NETWORKS	PP	100	40	19	50	69	P C
404183	MICROWAVE ENGINEERING	PP	100	40	16	43	59	P C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	22	53	75	P C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	22	49	71	P C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			31	P C
404188	PROJECT PHASE I	OR	50	20			46	P C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	41	58	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	51	73	P
404191D	SOFT COMPUTING	PP	100	40	20	42	62	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	22	42	64	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			25	P
404195	PROJECT PHASE II	TW	100	40			95	P
404195	PROJECT PHASE II	PR	50	20			45	P

GRAND TOTAL = 1055/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023107 RATHOD SANKET SUNIL ARUNA ,71633559L ,MITP ,B120023107

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	32	49	P	C
404182	COMPUTER NETWORKS	PP	100	40	19	37	56	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	07	36	43	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	17	28	45	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	14	35	49	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	19	43	62	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	35	58	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	47	66	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	21	28	49	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			42	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	P
404195	PROJECT PHASE II	TW	100	40			93	P	P
404195	PROJECT PHASE II	PR	50	20			45	P	P

GRAND TOTAL = 967/1500 , RESULT:FIRST CLASS

B120023108 RATHOR RAVIRAJ GANESH JYOTI ,71633560D ,MITP ,B120023108

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	28	50	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	49	70	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	24	48	72	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	25	64	89	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	22	34	56	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			46	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			41	P	C
404188	PROJECT PHASE I	OR	50	20			48	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	21	51	72	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	42	67	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	23	53	76	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	21	42	63	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			41	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	P
404195	PROJECT PHASE II	TW	100	40			98	P	P
404195	PROJECT PHASE II	PR	50	20			49	P	P

GRAND TOTAL = 1139/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023109 RAVEKAR SAISHWAR PURUSHOTTAM JAYSHRI ,71526895D ,MITP ,B120023109

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	15	28	43	P	C
404182	COMPUTER NETWORKS	PP	100	40	20	33	53	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	27	38	65	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	21	61	82	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	08	34	42	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	19	45	64	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	41	61	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	24	43	67	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	18	14	--	F	F
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	P
404195	PROJECT PHASE II	TW	100	40			89	P	P
404195	PROJECT PHASE II	PR	50	20			44	P	P

GRAND TOTAL = --/1500 , RESULT:FAILS

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023110 REHAN KHUSRO HAMDULAY AKILA ,71526896B ,MITP ,B120023110

## SEM.:1

404181 VLSI DESIGN & TECHNOLOGY PP 100 40 10 30\$ 40 P  
 404182 COMPUTER NETWORKS PP 100 40 13 29 42 P C  
 404183 MICROWAVE ENGINEERING PP 100 40 16 32 48 P C  
 404184C SOFTWARE DEFINED RADIO PP 100 40 21 47 68 P C  
 404185D ARTIFICIAL INTELLIGENCE PP 100 40 23 35 58 P C  
 404186 LAB PRACTICE I (CN & MWE) TW 50 20 38 P C  
 404186 LAB PRACTICE I (CN & MWE) OR 50 20 40 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) TW 50 20 38 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) PR 50 20 30 P C  
 404188 PROJECT PHASE I OR 50 20 38 P C

## SEM.:2

404189 MOBILE COMMUNICATION PP 100 40 15 41 56 P  
 404190 BROADBAND COMMUNICATION SYS. PP 100 40 22 35 57 P  
 404191D SOFT COMPUTING PP 100 40 19 33 52 P  
 404192EA ADV. AUTOMOTIVE ELECTRONICS PP 100 40 17 30 47 P  
 404193 LAB PRACTICE III(MC & BCS) TW 50 20 36 P  
 404193 LAB PRACTICE III(MC & BCS) OR 50 20 36 P  
 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 41 P  
 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 31 P  
 404195 PROJECT PHASE II TW 100 40 82 P  
 404195 PROJECT PHASE II PR 50 20 38 P

GRAND TOTAL = 916/1500 , RESULT:FIRST CLASS [\$ 0.1]

B120023111 REMANE KEDAR SHRINIVAS MRUNAL ,71526897L ,MITP ,B120023111

## SEM.:1

404181 VLSI DESIGN & TECHNOLOGY PP 100 40 12 29 41 P C  
 404182 COMPUTER NETWORKS PP 100 40 15 43 58 P C  
 404183 MICROWAVE ENGINEERING PP 100 40 19 45 64 P C  
 404184A DIGITAL IMAGE PROCESSING PP 100 40 29 41 70 P C  
 404185D ARTIFICIAL INTELLIGENCE PP 100 40 19 35 54 P C  
 404186 LAB PRACTICE I (CN & MWE) TW 50 20 43 P C  
 404186 LAB PRACTICE I (CN & MWE) OR 50 20 39 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) TW 50 20 40 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) PR 50 20 33 P C  
 404188 PROJECT PHASE I OR 50 20 46 P C

## SEM.:2

404189 MOBILE COMMUNICATION PP 100 40 20 48 68 P  
 404190 BROADBAND COMMUNICATION SYS. PP 100 40 21 52 73 P  
 404191D SOFT COMPUTING PP 100 40 18 44 62 P  
 404192EA ADV. AUTOMOTIVE ELECTRONICS PP 100 40 22 34 56 P  
 404193 LAB PRACTICE III(MC & BCS) TW 50 20 40 P  
 404193 LAB PRACTICE III(MC & BCS) OR 50 20 37 P  
 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 38 P  
 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 40 P  
 404195 PROJECT PHASE II TW 100 40 98 P  
 404195 PROJECT PHASE II PR 50 20 46 P

GRAND TOTAL = 1046/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023112 REVATI SHINDE GEETANJALI ,71526898J ,MITP ,B120023112

## SEM.:1

404181 VLSI DESIGN & TECHNOLOGY PP 100 40 14 28 42 P C  
 404182 COMPUTER NETWORKS PP 100 40 20 57 77 P C  
 404183 MICROWAVE ENGINEERING PP 100 40 24 52 76 P C  
 404184B EMBEDDED SYSTEMS & RTOS PP 100 40 23 45 68 P C  
 404185B ELECTRONIC PRODUCT DESIGN PP 100 40 24 46 70 P C  
 404186 LAB PRACTICE I (CN & MWE) TW 50 20 42 P C  
 404186 LAB PRACTICE I (CN & MWE) OR 50 20 41 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) TW 50 20 43 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) PR 50 20 38 P C  
 404188 PROJECT PHASE I OR 50 20 38 P C

## SEM.:2

404189 MOBILE COMMUNICATION PP 100 40 21 40 61 P  
 404190 BROADBAND COMMUNICATION SYS. PP 100 40 24 50 74 P  
 404191C AUDIO VIDEO ENGINEERING PP 100 40 25 46 71 P  
 404192EA ADV. AUTOMOTIVE ELECTRONICS PP 100 40 18 40 58 P  
 404193 LAB PRACTICE III(MC & BCS) TW 50 20 40 P  
 404193 LAB PRACTICE III(MC & BCS) OR 50 20 37 P  
 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 40 P  
 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 45 P  
 404195 PROJECT PHASE II TW 100 40 76 P  
 404195 PROJECT PHASE II PR 50 20 39 P

GRAND TOTAL = 1076/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023113 ROHIT NITIN GUJARATHI NAYANA ,71526908K ,MITP ,B120023113

SEM.:1								SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	20	35	55	P	C	404189	MOBILE COMMUNICATION	PP	100	40	23	47	70	P	P
404182	COMPUTER NETWORKS	PP	100	40	24	58	82	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	27	48	75	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	44	59	P	C	404191D	SOFT COMPUTING	PP	100	40	27	41	68	P	P
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	25	53	78	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	21	40	61	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			46	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			43	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			47	P	C	404195	PROJECT PHASE II	TW	100	40			95	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			43	P	C	404195	PROJECT PHASE II	PR	50	20			48	P	P
404188	PROJECT PHASE I	OR	50	20			45	P	C	414464D	INTERNET OF THING	PP	100	40	27	46	73	P	P

GRAND TOTAL = 1143/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023114 ROSHAN M V PUSHPA ,71526911K ,MITP ,B120023114

SEM.:1								SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	36	52	P		404189	MOBILE COMMUNICATION	PP	100	40	17	48	65	P	P
404182	COMPUTER NETWORKS	PP	100	40	21	37	58	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	54	80	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	47	66	P	C	404191D	SOFT COMPUTING	PP	100	40	19	41	60	P	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	29	45	74	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	22	36	58	P	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	20	49	69	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			41	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			43	P	C	404195	PROJECT PHASE II	TW	100	40			98	P	P
404188	PROJECT PHASE I	OR	50	20			45	P	C	404195	PROJECT PHASE II	PR	50	20			47	P	P

GRAND TOTAL = 1102/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023115 SAHEJPAL SINGH GREWAL SUVINDER KAUR GREWAL ,71526918G ,MITP ,B120023115

SEM.:1								SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	07	33	40	P	C	404189	MOBILE COMMUNICATION	PP	100	40	26	48	74	P	P
404182	COMPUTER NETWORKS	PP	100	40	18	51	69	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	39	63	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	19	38	57	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	27	48	75	P	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	18	56	74	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	P
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	22	46	68	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			44	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P	C	404195	PROJECT PHASE II	TW	100	40			98	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P	C	404195	PROJECT PHASE II	PR	50	20			48	P	P
404188	PROJECT PHASE I	OR	50	20			45	P	C	414464D	INTERNET OF THING	PP	100	40	29	53	82	P	P

GRAND TOTAL = 1117/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023116 SAHIL SHARMA SEEMA ,71526919E ,MITP ,B120023116

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	38	48	P	404189	MOBILE COMMUNICATION	PP	100	40	24	52	76	P
404182	COMPUTER NETWORKS	PP	100	40	18	41	59	P C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	17	48	65	P
404183	MICROWAVE ENGINEERING	PP	100	40	15	34	49	P C	404191D	SOFT COMPUTING	PP	100	40	17	40	57	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	19	60	79	P C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	26	42	68	P C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			33	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			29	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P C	404195	PROJECT PHASE II	TW	100	40			91	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P C	404195	PROJECT PHASE II	PR	50	20			47	P
404188	PROJECT PHASE I	OR	50	20			42	P C	414464D	INTERNET OF THING	PP	100	40	28	49	77	P

## SEM.:2

GRAND TOTAL = 1056/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023117 SALUNKE VEDANT VINOD ANITA ,71526930F ,MITP ,B120023117

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	32	42	P	404189	MOBILE COMMUNICATION	PP	100	40	18	40	58	P
404182	COMPUTER NETWORKS	PP	100	40	18	35	53	P C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	33	56	P
404183	MICROWAVE ENGINEERING	PP	100	40	10	38	48	P C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	15	40	55	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	17	38	55	P C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	15	31	46	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	16	37	53	P C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			32	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			28	P C	404195	PROJECT PHASE II	TW	100	40			94	P
404188	PROJECT PHASE I	OR	50	20			44	P C	404195	PROJECT PHASE II	PR	50	20			45	P

## SEM.:2

GRAND TOTAL = 939/1500 , RESULT:FIRST CLASS

B120023118 SANGAI SIDDHANT SANDEEP TRUPTI ,71526933L ,MITP ,B120023118

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	22	32	54	P C	404189	MOBILE COMMUNICATION	PP	100	40	18	39	57	P
404182	COMPUTER NETWORKS	PP	100	40	19	44	63	P C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	54	79	P
404183	MICROWAVE ENGINEERING	PP	100	40	21	44	65	P C	404191D	SOFT COMPUTING	PP	100	40	26	41	67	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	29	50	79	P C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	21	36	57	P
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	19	44	63	P C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			42	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			42	P C	404195	PROJECT PHASE II	TW	100	40			99	P
404188	PROJECT PHASE I	OR	50	20			48	P C	404195	PROJECT PHASE II	PR	50	20			49	P

## SEM.:2

GRAND TOTAL = 1108/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023119 SARANG JITENDRA BACHAL SUJATA ,71526942K ,MITP ,B120023119

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	31	41	P	C	404189	MOBILE COMMUNICATION	PP	100	40	16	36	52	P	
404182	COMPUTER NETWORKS	PP	100	40	19	53	72	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	48	71	P	
404183	MICROWAVE ENGINEERING	PP	100	40	23	46	69	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	23	47	70	P	
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	27	49	76	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	20	38	58	P	
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	22	48	70	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P	
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20				40	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20				44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20				43	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P	C	404195	PROJECT PHASE II	TW	100	40				98	P
404188	PROJECT PHASE I	OR	50	20			42	P	C	404195	PROJECT PHASE II	PR	50	20				48	P

GRAND TOTAL = 1092/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023120 SATPUTE SABHYATA AJIT SIMA ,71633561B ,MITP ,B120023120

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	37	48	P	C	404189	MOBILE COMMUNICATION	PP	100	40	22	43	65	P	
404182	COMPUTER NETWORKS	PP	100	40	16	41	57	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	46	70	P	
404183	MICROWAVE ENGINEERING	PP	100	40	17	48	65	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	47	66	P	
404184C	SOFTWARE DEFINED RADIO	PP	100	40	15	53	68	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	15	33	48	P	
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	14	44	58	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20				37	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20				34	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20				38	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20				40	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			31	P	C	404195	PROJECT PHASE II	TW	100	40				85	P
404188	PROJECT PHASE I	OR	50	20			38	P	C	404195	PROJECT PHASE II	PR	50	20				39	P

GRAND TOTAL = 1009/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023121 SATPUTE SHREYA BALASAHEB ANJALI ,71526946B ,MITP ,B120023121

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	58	71	P	C	404189	MOBILE COMMUNICATION	PP	100	40	24	45	69	P	
404182	COMPUTER NETWORKS	PP	100	40	20	48	68	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	61	83	P	
404183	MICROWAVE ENGINEERING	PP	100	40	26	57	83	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	28	55	83	P	
404184C	SOFTWARE DEFINED RADIO	PP	100	40	26	67	93	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	17	50	67	P	
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	21	37	58	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20				38	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20				36	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20				45	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20				44	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			28	P	C	404195	PROJECT PHASE II	TW	100	40				91	P
404188	PROJECT PHASE I	OR	50	20			41	P	C	404195	PROJECT PHASE II	PR	50	20				46	P

GRAND TOTAL = 1165/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023122 SAUMIL SRIVASTAVA SUMITA ,71526947L ,MITP ,B120023122

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	53	67	P	C
404182	COMPUTER NETWORKS	PP	100	40	22	47	69	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	59	80	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	30	50	80	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	24	47	71	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			48	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	21	50	71	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	55	79	P	P
404191D	SOFT COMPUTING	PP	100	40	24	43	67	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	26	40	66	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			36	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	P
404195	PROJECT PHASE II	TW	100	40			98	P	P
404195	PROJECT PHASE II	PR	50	20			47	P	P

GRAND TOTAL = 1160/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023123 SAURABH RAWAL JYOTSNA ,71526948J ,MITP ,B120023123

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	52	65	P	C
404182	COMPUTER NETWORKS	PP	100	40	15	33	48	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	45	64	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	19	46	65	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	24	46	70	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	24	48	72	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	49	74	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	47	65	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	P
404195	PROJECT PHASE II	TW	100	40			95	P	P
404195	PROJECT PHASE II	PR	50	20			44	P	P
414464D	INTERNET OF THING	PP	100	40	26	40	66	P	P

GRAND TOTAL = 1081/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023124 SAWARKAR SHUBHAM CHANDRASHEKAR MAYA ,71633562L ,MITP ,B120023124

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	16	49	65	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	39	55	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	17	40	57	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	25	46	71	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	10	30	40	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			43	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	23	43	66	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	49	71	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	22	51	73	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	21	41	62	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			40	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			35	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			37	P	P
404195	PROJECT PHASE II	TW	100	40			91	P	P
404195	PROJECT PHASE II	PR	50	20			45	P	P

GRAND TOTAL = 1058/1500 , RESULT:FIRST CLASS WITH DISTINCTION



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023125 SAYKAR ARCHANA MARUTI SUNITA ,71633563J ,MITP ,B120023125

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	38	55	P	C	404189	MOBILE COMMUNICATION	PP	100	40	19	37	56	P	P
404182	COMPUTER NETWORKS	PP	100	40	20	31	51	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	46	68	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	23	55	78	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	45	63	P	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	21	57	78	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	15	29	44	P	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	10	37	47	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			37	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			45	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			42	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			32	P	C	404195	PROJECT PHASE II	TW	100	40			85	P	P
404188	PROJECT PHASE I	OR	50	20			40	P	C	404195	PROJECT PHASE II	PR	50	20			40	P	P

GRAND TOTAL = 1022/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023126 SHEJUL SHRADHA ANKUSH SHUBHANGI ,71526969M ,MITP ,B120023126

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	61	75	P	C	404189	MOBILE COMMUNICATION	PP	100	40	24	39	63	P	P
404182	COMPUTER NETWORKS	PP	100	40	21	37	58	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	45	70	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	25	56	81	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	23	53	76	P	P
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	28	50	78	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	23	39	62	P	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	10	48	58	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			39	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			45	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			45	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P	C	404195	PROJECT PHASE II	TW	100	40			98	P	P
404188	PROJECT PHASE I	OR	50	20			46	P	C	404195	PROJECT PHASE II	PR	50	20			48	P	P

GRAND TOTAL = 1146/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023127 SHINDE RAHUL DATTATRAYA NIRMALA ,71526973K ,MITP ,B120023127

SEM.:1										SEM.:2									
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	46	63	P	C	404189	MOBILE COMMUNICATION	PP	100	40	22	54	76	P	P
404182	COMPUTER NETWORKS	PP	100	40	24	45	69	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	48	72	P	P
404183	MICROWAVE ENGINEERING	PP	100	40	22	58	80	P	C	404191C	AUDIO VIDEO ENGINEERING	PP	100	40	24	45	69	P	P
404184C	SOFTWARE DEFINED RADIO	PP	100	40	26	57	83	P	C	404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	29	42	71	P	P
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	29	50	79	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	P
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	P
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			42	P	P
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			28	P	C	404195	PROJECT PHASE II	TW	100	40			90	P	P
404188	PROJECT PHASE I	OR	50	20			40	P	C	404195	PROJECT PHASE II	PR	50	20			40	P	P

GRAND TOTAL = 1141 + 05/1500 , RESULT:FIRST CLASS WITH DISTINCTION [& 0.163]

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023128 SHINDE SURAJ LAXMAN MINA ,71526975F ,MITP ,B120023128

## SEM.:1

404181 VLSI DESIGN & TECHNOLOGY PP 100 40 13 54 67 P C  
 404182 COMPUTER NETWORKS PP 100 40 22 47 69 P C  
 404183 MICROWAVE ENGINEERING PP 100 40 23 54 77 P C  
 404184C SOFTWARE DEFINED RADIO PP 100 40 28 54 82 P C  
 404185A MULTI RATE & ADAP SIGN. PROC PP 100 40 12 44 56 P C  
 404186 LAB PRACTICE I (CN & MWE) TW 50 20 44 P C  
 404186 LAB PRACTICE I (CN & MWE) OR 50 20 40 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) TW 50 20 41 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) PR 50 20 28 P C  
 404188 PROJECT PHASE I OR 50 20 38 P C

## SEM.:2

404189 MOBILE COMMUNICATION PP 100 40 23 48 71 P  
 404190 BROADBAND COMMUNICATION SYS. PP 100 40 16 48 64 P  
 404191C AUDIO VIDEO ENGINEERING PP 100 40 24 52 76 P  
 404192EA ADV. AUTOMOTIVE ELECTRONICS PP 100 40 21 36 57 P  
 404193 LAB PRACTICE III(MC & BCS) TW 50 20 38 P  
 404193 LAB PRACTICE III(MC & BCS) OR 50 20 36 P  
 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 40 P  
 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 39 P  
 404195 PROJECT PHASE II TW 100 40 92 P  
 404195 PROJECT PHASE II PR 50 20 42 P

GRAND TOTAL = 1097/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023129 SHIVSHARAN SHEKHAR GAUTAM SWATI ,71100614J ,MITP ,B120023129

## SEM.:1

404181 VLSI DESIGN & TECHNOLOGY PP 100 40 13 48 61 P C  
 404182 COMPUTER NETWORKS PP 100 40 15 30 45 P C  
 404183 MICROWAVE ENGINEERING PP 100 40 14 43 57 P C  
 404184C SOFTWARE DEFINED RADIO PP 100 40 18 35 53 P C  
 404185A MULTI RATE & ADAP SIGN. PROC PP 100 40 07 45 52 P C  
 404186 LAB PRACTICE I (CN & MWE) TW 50 20 42 P C  
 404186 LAB PRACTICE I (CN & MWE) OR 50 20 40 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) TW 50 20 42 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) PR 50 20 23 P C  
 404188 PROJECT PHASE I OR 50 20 32 P C

## SEM.:2

404189 MOBILE COMMUNICATION PP 100 40 18 34 52 P  
 404190 BROADBAND COMMUNICATION SYS. PP 100 40 16 35 51 P  
 404191C AUDIO VIDEO ENGINEERING PP 100 40 15 39 54 P  
 404192EA ADV. AUTOMOTIVE ELECTRONICS PP 100 40 13 23# 36# P  
 404193 LAB PRACTICE III(MC & BCS) TW 50 20 39 P  
 404193 LAB PRACTICE III(MC & BCS) OR 50 20 33 P  
 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 42 P  
 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 39 P  
 404195 PROJECT PHASE II TW 100 40 92 P  
 404195 PROJECT PHASE II PR 50 20 40 P

GRAND TOTAL = 925/1500 , RESULT:FIRST CLASS [# 0.4]

B120023130 SHRIPAD SHUBHAM SUDHAKAR VAISHALI ,71526981L ,MITP ,B120023130

## SEM.:1

404181 VLSI DESIGN & TECHNOLOGY PP 100 40 08 38 46 P C  
 404182 COMPUTER NETWORKS PP 100 40 13 37 50 P C  
 404183 MICROWAVE ENGINEERING PP 100 40 15 40 55 P C  
 404184A DIGITAL IMAGE PROCESSING PP 100 40 17 29 46 P C  
 404185D ARTIFICIAL INTELLIGENCE PP 100 40 20 30 50 P C  
 404186 LAB PRACTICE I (CN & MWE) TW 50 20 39 P C  
 404186 LAB PRACTICE I (CN & MWE) OR 50 20 39 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) TW 50 20 34 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) PR 50 20 35 P  
 404188 PROJECT PHASE I OR 50 20 37 P C

## SEM.:2

404189 MOBILE COMMUNICATION PP 100 40 18 37 55 P  
 404190 BROADBAND COMMUNICATION SYS. PP 100 40 18 36 54 P  
 404191C AUDIO VIDEO ENGINEERING PP 100 40 15 40 55 P  
 404192EA ADV. AUTOMOTIVE ELECTRONICS PP 100 40 14 28 42 P  
 404193 LAB PRACTICE III(MC & BCS) TW 50 20 36 P  
 404193 LAB PRACTICE III(MC & BCS) OR 50 20 32 P  
 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 34 P  
 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 36 P  
 404195 PROJECT PHASE II TW 100 40 75 P  
 404195 PROJECT PHASE II PR 50 20 39 P

GRAND TOTAL = 889/1500 , RESULT:HIGHER SECOND CLASS

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023131 SIDDHANT SUD DIVYA ,71526994B ,MITP ,B120023131

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	46	60	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	48	65	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	17	45	62	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	25	48	73	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	17	40	57	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			28	P	C
404188	PROJECT PHASE I	OR	50	20			44	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	16	37	53	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	18	48	66	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	19	42	61	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	21	37	58	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			41	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			45	P	

GRAND TOTAL = 1040/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023132 SIDDHARTH RAVINDRA INAMDAR SANGEETA RAVINDRA INAMDAR ,71300629D ,MITP ,B120023132

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	32	45	P	C
404182	COMPUTER NETWORKS	PP	100	40	14	30	44	P	
404183	MICROWAVE ENGINEERING	PP	100	40	15	29	44	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	17	38	55	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	14	42	56	P	
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P	C
404188	PROJECT PHASE I	OR	50	20			42	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	33	50	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	19	31	50	P	
404191D	SOFT COMPUTING	PP	100	40	16	35	51	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	
404195	PROJECT PHASE II	TW	100	40			95	P	
404195	PROJECT PHASE II	PR	50	20			47	P	
414464D	INTERNET OF THING	PP	100	40	21	35	56	P	

GRAND TOTAL = 935/1500 , RESULT:FIRST CLASS

RESULT RESERVED FOR BACKLOGS.

B120023133 SRIVASTAVA SUYASH CHANDRANATH REENA ,71527003G ,MITP ,B120023133

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	39	53	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	44	60	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	41	63	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	24	45	69	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	19	34	53	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			38	P	C
404188	PROJECT PHASE I	OR	50	20			48	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	23	35	58	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	22	38	60	P	
404191D	SOFT COMPUTING	PP	100	40	16	35	51	P	
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	13	38	51	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			34	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	
404195	PROJECT PHASE II	TW	100	40			98	P	
404195	PROJECT PHASE II	PR	50	20			47	P	

GRAND TOTAL = 1013/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023134 SUJEET VINAYAK PATHADE JYOTI ,71527006M ,MITP ,B120023134

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	46	57	P	C
404182	COMPUTER NETWORKS	PP	100	40	19	36	55	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	50	71	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	17	51	68	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	09	31	40	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C
404188	PROJECT PHASE I	OR	50	20			35	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	24	38	62	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	23	44	67	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	45	63	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	14	30	44	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			39	P	P
404195	PROJECT PHASE II	TW	100	40			92	P	P
404195	PROJECT PHASE II	PR	50	20			41	P	P

GRAND TOTAL = 998/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023135 TANMANE KAJAL SANTOSH PUSHPA ,71633564G ,MITP ,B120023135

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	61	75	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	46	63	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	26	57	83	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	25	59	84	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	12	48	60	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			42	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			28	P	C
404188	PROJECT PHASE I	OR	50	20			34	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	26	46	72	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	25	49	74	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	23	51	74	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	18	30	48	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			43	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	P
404195	PROJECT PHASE II	TW	100	40			82	P	P
404195	PROJECT PHASE II	PR	50	20			39	P	P

GRAND TOTAL = 1094/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023136 TEJESH LOHIYA KALPANA ,71527028B ,MITP ,B120023136

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	43	60	P	C
404182	COMPUTER NETWORKS	PP	100	40	16	44	60	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	19	51	70	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	08	38	46	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	09	44	53	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			33	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	24	43	67	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	45	71	P	P
404191D	SOFT COMPUTING	PP	100	40	21	41	62	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			38	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			25	P	P
404195	PROJECT PHASE II	TW	100	40			91	P	P
404195	PROJECT PHASE II	PR	50	20			42	P	P
414464D	INTERNET OF THING	PP	100	40	29	50	79	P	P

GRAND TOTAL = 1019/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023137 THAKARE VAIBHAV SHANKAR MAYA ,71633565E ,MITP ,B120023137

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	19	61	80	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	44	65	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	26	53	79	P	C
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	22	52	74	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	26	48	74	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			48	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			45	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			48	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			47	P	C
404188	PROJECT PHASE I	OR	50	20			43	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	21	44	65	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	47	73	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	25	48	73	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	18	32	50	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			45	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			44	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			45	P	P
404195	PROJECT PHASE II	TW	100	40			95	P	P
404195	PROJECT PHASE II	PR	50	20			45	P	P

GRAND TOTAL = 1178/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023138 TIWARI ROHIT SAROJA ,71527035E ,MITP ,B120023138

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	53	66	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	47	70	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	21	49	70	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	24	53	77	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	20	48	68	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	17	36	53	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	27	51	78	P	P
404191D	SOFT COMPUTING	PP	100	40	18	37	55	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	18	28	46	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			37	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			37	P	P
404195	PROJECT PHASE II	TW	100	40			89	P	P
404195	PROJECT PHASE II	PR	50	20			44	P	P

GRAND TOTAL = 1060/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023140 VAZE PRANAV KEDAR KIRTI ,71527049E ,MITP ,B120023140

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	13	51	64	P	C
404182	COMPUTER NETWORKS	PP	100	40	24	38	62	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	25	40	65	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	23	49	72	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	21	44	65	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			45	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			40	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			37	P	C
404188	PROJECT PHASE I	OR	50	20			46	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	24	40	64	P	P
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	26	49	75	P	P
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	17	45	62	P	P
404192EA	ADV. AUTOMOTIVE ELECTRONICS	PP	100	40	20	38	58	P	P
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	P
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	P
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	P
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			38	P	P
404195	PROJECT PHASE II	TW	100	40			98	P	P
404195	PROJECT PHASE II	PR	50	20			48	P	P

GRAND TOTAL = 1095/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023141 VEDANG SATISH SAWANT SNEHAL ,71527050J ,MITP ,B120023141

## SEM.:1

404181 VLSI DESIGN & TECHNOLOGY PP 100 40 15 44 59 P C  
 404182 COMPUTER NETWORKS PP 100 40 16 36 52 P C  
 404183 MICROWAVE ENGINEERING PP 100 40 20 33 53 P C  
 404184A DIGITAL IMAGE PROCESSING PP 100 40 26 42 68 P C  
 404185A MULTI RATE & ADAP SIGN. PROC PP 100 40 17 43 60 P C  
 404186 LAB PRACTICE I (CN & MWE) TW 50 20 42 P C  
 404186 LAB PRACTICE I (CN & MWE) OR 50 20 40 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) TW 50 20 44 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) PR 50 20 38 P C  
 404188 PROJECT PHASE I OR 50 20 44 P C

## SEM.:2

404189 MOBILE COMMUNICATION PP 100 40 15 46 61 P  
 404190 BROADBAND COMMUNICATION SYS. PP 100 40 22 46 68 P  
 404191C AUDIO VIDEO ENGINEERING PP 100 40 23 53 76 P  
 404192EA ADV. AUTOMOTIVE ELECTRONICS PP 100 40 18 32 50 P  
 404193 LAB PRACTICE III(MC & BCS) TW 50 20 40 P  
 404193 LAB PRACTICE III(MC & BCS) OR 50 20 37 P  
 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 45 P  
 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 39 P  
 404195 PROJECT PHASE II TW 100 40 98 P  
 404195 PROJECT PHASE II PR 50 20 46 P

GRAND TOTAL = 1060/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023142 WANI GAURAV RAJENDRA SUCHITA ,71527061D ,MITP ,B120023142

## SEM.:1

404181 VLSI DESIGN & TECHNOLOGY PP 100 40 09 50 59 P C  
 404182 COMPUTER NETWORKS PP 100 40 17 38 55 P C  
 404183 MICROWAVE ENGINEERING PP 100 40 14 36 50 P C  
 404184C SOFTWARE DEFINED RADIO PP 100 40 18 44 62 P C  
 404185A MULTI RATE & ADAP SIGN. PROC PP 100 40 17 34 51 P C  
 404186 LAB PRACTICE I (CN & MWE) TW 50 20 43 P C  
 404186 LAB PRACTICE I (CN & MWE) OR 50 20 40 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) TW 50 20 41 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) PR 50 20 35 P C  
 404188 PROJECT PHASE I OR 50 20 40 P C

## SEM.:2

404189 MOBILE COMMUNICATION PP 100 40 15 34 49 P  
 404190 BROADBAND COMMUNICATION SYS. PP 100 40 17 41 58 P  
 404191C AUDIO VIDEO ENGINEERING PP 100 40 16 42 58 P  
 404192EA ADV. AUTOMOTIVE ELECTRONICS PP 100 40 14 21# 35# P  
 404193 LAB PRACTICE III(MC & BCS) TW 50 20 39 P  
 404193 LAB PRACTICE III(MC & BCS) OR 50 20 35 P  
 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 38 P  
 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 40 P  
 404195 PROJECT PHASE II TW 100 40 90 P  
 404195 PROJECT PHASE II PR 50 20 40 P

GRAND TOTAL = 958/1500 , RESULT:FIRST CLASS [# 0.4]

B120023143 WANI UMANG NITIN SEEMA ,71527064J ,MITP ,B120023143

## SEM.:1

404181 VLSI DESIGN & TECHNOLOGY PP 100 40 15 48 63 P C  
 404182 COMPUTER NETWORKS PP 100 40 23 49 72 P C  
 404183 MICROWAVE ENGINEERING PP 100 40 20 44 64 P C  
 404184A DIGITAL IMAGE PROCESSING PP 100 40 26 48 74 P C  
 404185D ARTIFICIAL INTELLIGENCE PP 100 40 22 42 64 P C  
 404186 LAB PRACTICE I (CN & MWE) TW 50 20 44 P C  
 404186 LAB PRACTICE I (CN & MWE) OR 50 20 40 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) TW 50 20 43 P C  
 404187 LAB PRAC. II(VLSI &ELEC. I) PR 50 20 42 P C  
 404188 PROJECT PHASE I OR 50 20 48 P C

## SEM.:2

404189 MOBILE COMMUNICATION PP 100 40 24 28 52 P  
 404190 BROADBAND COMMUNICATION SYS. PP 100 40 24 49 73 P  
 404191D SOFT COMPUTING PP 100 40 24 43 67 P  
 404192EA ADV. AUTOMOTIVE ELECTRONICS PP 100 40 22 32 54 P  
 404193 LAB PRACTICE III(MC & BCS) TW 50 20 40 P  
 404193 LAB PRACTICE III(MC & BCS) OR 50 20 38 P  
 404194 LAB PRAC. IV(ELECTIVE III) TW 50 20 41 P  
 404194 LAB PRAC. IV(ELECTIVE III) PR 50 20 39 P  
 404195 PROJECT PHASE II TW 100 40 99 P  
 404195 PROJECT PHASE II PR 50 20 49 P

GRAND TOTAL = 1106/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023144 WARANG SHRUTI SURYAKANT SAMIDHA ,71527068M ,MITP ,B120023144

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	47	61	P	C
404182	COMPUTER NETWORKS	PP	100	40	21	43	64	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	22	51	73	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	25	58	83	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	23	41	64	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			42	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			43	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C
404188	PROJECT PHASE I	OR	50	20			45	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	24	39	63	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	43	67	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	22	44	66	P	
404192D	WIRELESS NETWORKS	PP	100	40	21	48	69	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			41	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			37	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			44	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			44	P	
404195	PROJECT PHASE II	TW	100	40			98	P	
404195	PROJECT PHASE II	PR	50	20			48	P	

GRAND TOTAL = 1124/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023145 WAYASE SATYEN SANJAY VARSHA ,71527069K ,MITP ,B120023145

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	45	59	P	C
404182	COMPUTER NETWORKS	PP	100	40	23	43	66	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	14	31	45	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	23	45	68	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	16	44	60	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			33	P	C
404188	PROJECT PHASE I	OR	50	20			41	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	15	44	59	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	20	40	60	P	
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	25	45	70	P	
404192D	WIRELESS NETWORKS	PP	100	40	21	44	65	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			43	P	
404195	PROJECT PHASE II	TW	100	40			94	P	
404195	PROJECT PHASE II	PR	50	20			46	P	

GRAND TOTAL = 1041/1500 , RESULT:FIRST CLASS WITH DISTINCTION

B120023146 ZAVAR YASH ATUL DEEPIKA ,71527075D ,MITP ,B120023146

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	17	45	62	P	C
404182	COMPUTER NETWORKS	PP	100	40	25	45	70	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	24	53	77	P	C
404184A	DIGITAL IMAGE PROCESSING	PP	100	40	28	39	67	P	C
404185D	ARTIFICIAL INTELLIGENCE	PP	100	40	22	40	62	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			44	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			40	P	C
404188	PROJECT PHASE I	OR	50	20			40	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	22	41	63	P	
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	24	51	75	P	
404191D	SOFT COMPUTING	PP	100	40	21	55	76	P	
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			40	P	
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			40	P	
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			40	P	
404195	PROJECT PHASE II	TW	100	40			94	P	
404195	PROJECT PHASE II	PR	50	20			43	P	
414464D	INTERNET OF THING	PP	100	40	27	45	72	P	

GRAND TOTAL = 1126/1500 , RESULT:FIRST CLASS WITH DISTINCTION

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023148 DEVESH LAKHANPAL SONIA PAUL ,71300172M ,MITP ,B120023148

SEM.:1								SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	11	38	49	P	C	404189	MOBILE COMMUNICATION	PP	100	40	08	42	50	P	C
404182	COMPUTER NETWORKS	PP	100	40	17	35	52	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	19	37	56	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	12	32	44	P		404191C	AUDIO VIDEO ENGINEERING	PP	100	40	13	29	42	P	C
404184C	SOFTWARE DEFINED RADIO	PP	100	40	23	41	64	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	28	29	57	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			36	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			37	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			41	P	C	404195	PROJECT PHASE II	TW	100	40			96	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			30	P	C	404195	PROJECT PHASE II	PR	50	20			44	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C	414464D	INTERNET OF THING	PP	100	40	28	32	60	P	C

GRAND TOTAL = 954/1500 , RESULT:FIRST CLASS

B120023154 MOHAMMAD FIROZ RANGWALA MALIKA ,71200402F ,MITP ,B120023154

SEM.:1								SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	12	36	48	P	C	404189	MOBILE COMMUNICATION	PP	100	40	21	47	68	P	
404182	COMPUTER NETWORKS	PP	100	40	16	30	46	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	21	38	59	P	
404183	MICROWAVE ENGINEERING	PP	100	40	03	33	36#	P		404191C	AUDIO VIDEO ENGINEERING	PP	100	40	18	42	60	P	
404184C	SOFTWARE DEFINED RADIO	PP	100	40	15	38	53	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			37	P	
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	10	32	42	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			35	P	
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			40	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			41	P	
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			39	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			35	P	
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			36	P	C	404195	PROJECT PHASE II	TW	100	40			92	P	
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			25	P	C	404195	PROJECT PHASE II	PR	50	20			43	P	
404188	PROJECT PHASE I	OR	50	20			38	P	C	414464D	INTERNET OF THING	PP	100	40	27	48	75	P	

GRAND TOTAL = 948/1500 , RESULT:FIRST CLASS [# 0.4]

B120023156 PRANAV DESHPANDE VISHAKHA DESHPANDE ,71300497F ,MITP ,B120023156

SEM.:1								SEM.:2											
404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	09	AA	--	F		404189	MOBILE COMMUNICATION	PP	100	40	08	AA	--	F	
404182	COMPUTER NETWORKS	PP	100	40	12	30	42	P	C	404190	BROADBAND COMMUNICATION SYS.	PP	100	40	17	37	54	P	
404183	MICROWAVE ENGINEERING	PP	100	40	07	AA	--	F		404191D	SOFT COMPUTING	PP	100	40	14	AA	--	F	
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	21	29	50	P	C	404192D	WIRELESS NETWORKS	PP	100	40	14	AA	--	F	
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	25	44	69	P	C	404193	LAB PRACTICE III(MC & BCS)	TW	50	20			36	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			43	P	C	404193	LAB PRACTICE III(MC & BCS)	OR	50	20			34	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			41	P	C	404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			24	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			33	P	C	404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			24	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			34	P	C	404195	PROJECT PHASE II	TW	100	40			95	P	C
404188	PROJECT PHASE I	OR	50	20			44	P	C	404195	PROJECT PHASE II	PR	50	20			45	P	C

GRAND TOTAL = --/1500 , RESULT:FAILS

RESULT RESERVED FOR BACKLOGS.



NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN.PASS MARKS, INT. MARKS, TH. MARKS, TOTAL MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

B120023158 SALUNKHE RANJIT SHARAD ANJALI ,71236299B ,MITP ,B120023158

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	14	46	60	P	C
404182	COMPUTER NETWORKS	PP	100	40	14	28\$	42	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	10	30\$	40	P	
404184B	EMBEDDED SYSTEMS & RTOS	PP	100	40	10	44	54	P	C
404185B	ELECTRONIC PRODUCT DESIGN	PP	100	40	07	37	44	P	C
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			44	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			32	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			35	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			28	P	C
404188	PROJECT PHASE I	OR	50	20			36	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	05	47	52	P	C
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	11	29\$	40	P	C
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	17	36	53	P	C
404192D	WIRELESS NETWORKS	PP	100	40	15	42	57	P	C
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			39	P	C
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	C
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			31	P	C
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			30	P	C
404195	PROJECT PHASE II	TW	100	40			79	P	C
404195	PROJECT PHASE II	PR	50	20			42	P	C

GRAND TOTAL = 876/1500 , RESULT:HIGHER SECOND CLASS [\$ O.1]

B120023184 INGLE SUMEDH JALINDAR KAVITA ,71200230J ,MITP ,B120023184

## SEM.:1

404181	VLSI DESIGN & TECHNOLOGY	PP	100	40	10	35	45	P	C
404182	COMPUTER NETWORKS	PP	100	40	18	28	46	P	C
404183	MICROWAVE ENGINEERING	PP	100	40	05	29	34#	P	
404184C	SOFTWARE DEFINED RADIO	PP	100	40	20	38	58	P	C
404185A	MULTI RATE & ADAP SIGN. PROC	PP	100	40	07	40	47	P	
404186	LAB PRACTICE I (CN & MWE)	TW	50	20			39	P	C
404186	LAB PRACTICE I (CN & MWE)	OR	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	TW	50	20			38	P	C
404187	LAB PRAC. II(VLSI &ELEC. I)	PR	50	20			36	P	C
404188	PROJECT PHASE I	OR	50	20			38	P	C

## SEM.:2

404189	MOBILE COMMUNICATION	PP	100	40	08	39	47	P	C
404190	BROADBAND COMMUNICATION SYS.	PP	100	40	14	28	42	P	C
404191C	AUDIO VIDEO ENGINEERING	PP	100	40	16	28	44	P	C
404193	LAB PRACTICE III(MC & BCS)	TW	50	20			38	P	C
404193	LAB PRACTICE III(MC & BCS)	OR	50	20			38	P	C
404194	LAB PRAC. IV(ELECTIVE III)	TW	50	20			34	P	C
404194	LAB PRAC. IV(ELECTIVE III)	PR	50	20			33	P	C
404195	PROJECT PHASE II	TW	100	40			87	P	C
404195	PROJECT PHASE II	PR	50	20			43	P	C
414464D	INTERNET OF THING	PP	100	40	22	32	54	P	C

GRAND TOTAL = 879/1500 , RESULT:HIGHER SECOND CLASS [# O.4]

RESULT RESERVED FOR BACKLOGS.