

MAEER's
Mahatrashta Institute of
Technology, Pune

Department of Computer Engineering



SuperComputing with
Param-Shavak

DEPARTMENT OF COMPUTER ENGINEERING

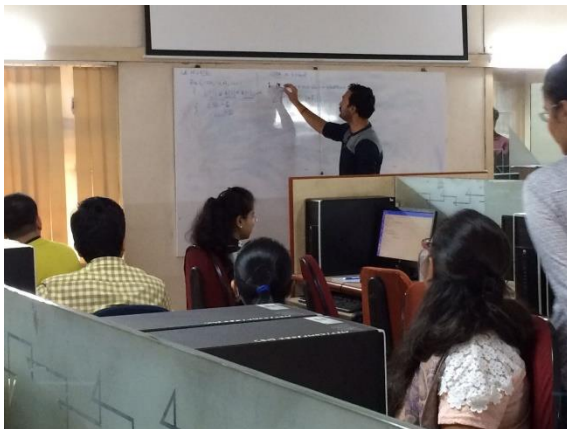
Add-On Course| August -September 2017

Report

High Performance Computing Add-on Course

High-Performance Computing (HPC) most generally refers to the practice of aggregating computing power in a way that delivers much higher performance than one could get out of a typical desktop computer or workstation in-order to solve large problems in science, engineering, or business. The idea of this add-on emerged from the sole perspective of giving students exposure about the parallel programming and HPC platforms like PARAM-SHAVAK.

Department of Computer Engineering organized this add-on course on High Performance Computing for students of Third and Final Year Engineering. The course was conducted in association with Tata Consultancy Services (TCS), Pune and Centre for Development of Advanced (C-DAC), Pune. It was a 20 hrs course. Hands on session on CUDA was conducted in SL-III lab of the department, which gave students insight of parallel programming using Graphics cards. Whereas, the hands-on session on Supercomputer PARAM-SHAVAK was conducted by C-DAC spokesperson in IBM lab of the department.



Total 32 students had participated for the course. The course was organized by Prof. Prasad Purnaye, Dr. Nitin Pise and Prof. Madhura Phatak. Course is successfully declared completed on 16th September. The teaching staff have received positive feedback about the course.